

DIO1266

Over-Voltage Protection Load Switch

Features

- Surge Protection
IEC 61000-4-5: 80V
- Input Clamping Voltage: 42V (Typ)
- Over-Voltage Protection (OVP)
- Over-Temperature Protection (OTP)
- ESD Protection
IEC 61000-4-2 Air Discharge: > 15kV
IEC 61000-4-2 Contact Discharge: > 8kV
- Negative Voltage Protection(-6V)

Descriptions

The DIO1266 features a low- R_{ON} internal FET and an operating range of 2.5 V_{DC} to 40 V_{DC} (absolute maximum of 42 V_{DC}). The DIO1266 features over-voltage protection that powers down the internal FET if the input voltage exceeds the OVP threshold. The OVP threshold is adjustable with optional external resistors. Over-temperature protection also powers down the device at 130°C (typical).

The DIO1266 is available in WLCSP-12 Package.

Applications

- Mobile Handsets and Tablets
- Portable Media Players
- MP3 Players

Function Block Diagram

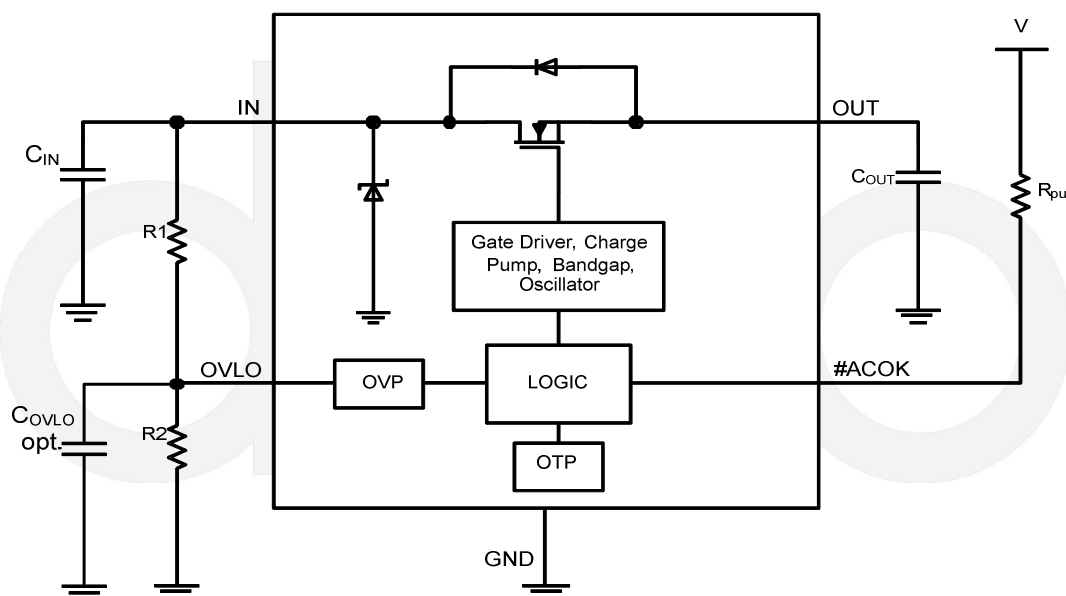


Figure 1. Functional Block Diagram



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Ordering Information

Order Part Number	Top Marking		T _A	Package	
DIO1266WL12	DM6F	Green	-40 to 85°C	WLCSP-12 0.4mm pitch	Tape & Reel, 3000

Pin Configuration

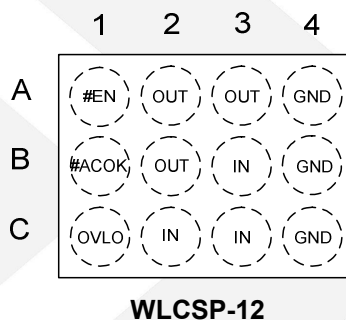


Figure 2. Pin Assignment (Top View)

Pin Definitions

Name	Bump	Type	Description		
IN	B3,C2,C3	Input/Supply	Switch Input and Device Supply		
OUT	A2,A3,B2	Output	Switch Output to Load		
#ACOK	B1	Output	Power Good	1	$V_{IN} < V_{IN_min}$ or $V_{IN} \geq V_{OVLO}$
				0	Voltage Stable
#EN	A1	Input	Device Enable, Low is enable.		
OVLO	C1	Input	Over-Voltage Lockout Adjustment Pin		
GND	A4,B4,C4	Supply	Device Ground		



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Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Rating” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter		Rating	Unit
V_{IN}	V_{IN} to GND ⁽¹⁾		-6 to 42	V
V_{OUT}	V_{OUT} to GND		-0.3 to $V_{IN}+0.3$	V
V_{OVLO}	OVLO to GND		-0.3 to 30	V
$V_{\#EN_ACOK}$	Maximum DC Voltage Allowed on #EN or ACOK Pin		6	V
I_{IN}	Switch I/O Current (Continuous)		4.5	A
T_{STG}	Storage Temperature Range		-65 to 150	°C
T_J	Maximum Junction Temperature		150	°C
T_L	Lead Temperature (Soldering, 10 Seconds)		260	°C
θ_{JA}	Thermal Resistance, Junction-to-Ambient		84.1	°C/W
ESD	IEC 61000-4-2 System ESD	Air Gap	15.0	kV
		Contact	8.0	
Surge ⁽²⁾	IEC 61000-4-5, Surge Protection	V_{IN}	80	V

(1) Maximum reverse current to V_{IN} pin less than 6A.

(2) No load

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min	Typ.	Max	Unit
V_{IN}	Supply Voltage	2.5		40	V
T_A	Operating Temperature	-40		85	°C
I_{OUT}	Output Current			4	A



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Electrical Characteristics

$T_A = -40^{\circ}\text{C}$ to 85°C , unless otherwise specified. Typical values are $V_{IN}=5.0\text{V}$, $I_{IN}\leq 3\text{A}$, $C_{IN}=0.1\mu\text{F}$ and $T_A=25^{\circ}\text{C}$.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{IN_CLAMP}	Input Clamping Voltage	$I_{IN}=10\text{mA}$		42		V
I_Q	Input Quiescent Current	$V_{IN}=5\text{V}$, $\#EN=0\text{V}$		80	130	μA
I_{IN_Q}	OVLO Supply Current	$V_{OVLO}=3\text{V}$, $V_{IN}=5\text{V}$, $V_{OUT}=0\text{V}$		70	96	μA
V_{UVLO}	Under Voltage Trip Level	V_{IN} Rising		2.25	2.4	V
		V_{IN} Falling		1.95	2.1	V
V_{IN_OVLO}	Internal Over-Voltage Trip Level	V_{IN} Rising, $OVLO=GND$	6.7	6.8	6.9	V
		V_{IN} Falling	6.2			V
V_{OVLO_TH}	OVLO Set Threshold	$V_{IN}=2.5\text{V}$ to V_{OVLO}	1.18	1.20	1.22	V
V_{OVLO_RNG}	Adjustable OVLO Threshold Range	$V_{IN}=2.5\text{V}$ to V_{OVLO}	4		22	V
V_{OVLO_SELECT}	External OVLO Select Threshold		0.28	0.30	0.32	V
R_{ON}	Resistance from V_{IN} to V_{OUT}	$V_{IN}=5\text{V}$, $I_{OUT}=1\text{A}$, $T_A=25^{\circ}\text{C}$		35	40	$\text{m}\Omega$
C_{OUT}	OUT Load Capacitance	$V_{IN}=5\text{V}$			1000	μF
I_{OLVO}	OVLO Input Leakage Current	$V_{OVLO}=V_{OVLO_TH}$	-100		100	nA
T_{SDN}	Thermal Shutdown			130		$^{\circ}\text{C}$
T_{SDN_HYS}	Thermal Shutdown Hysteresis			20		$^{\circ}\text{C}$
Digital Signals						
V_{OL}	#ACOK Output Low Voltage	$I_{SINK}=1\text{mA}$			0.4	V
$V_{IH_}\#EN$	Enable HIGH Voltage	$V_{IN}=2.5\text{V}$ to V_{OVLO}	1.2			V
$V_{IL_}\#EN$	Enable LOW Voltage	$V_{IN}=2.5\text{V}$ to V_{OVLO}			0.5	V
I_{ACOK_LEAK}	#ACOK Leakage Current	$V_{ACOK}=3\text{V}$, #ACOK Deasserted	-0.5		0.5	μA
$\#EN_Leak$	#EN Leakage Current	$V_{IN}=5.0\text{V}$, $V_{OUT}=Float$	-1.0		1.0	μA
Timing Characteristics						
t_{DEB}	Debounce Time	Time from $2.5\text{V}<V_{IN}<V_{IN_OVLO}$ to $V_{OUT}=0.1 \times V_{IN}$		15		ms
t_{START}	Soft-Start Time	Time from $V_{IN}=V_{IN_min}$ to $0.2 \times \#ACOK$, $V_{IO}=1.8\text{V}$ with $10\text{k}\Omega$ Pull-up Resistor		30		ms
t_{ON}	Switch Turn-On Time (Note 1)	$R_L=100\Omega$, $C_L=22\mu\text{F}$, V_{OUT} from $0.1 \times V_{IN}$ to $0.9 \times V_{IN}$	1.5	2	4	ms
t_{OFF}	Switch Turn-Off Time (Note 1)	$R_L=100\Omega$, $C_L=0\mu\text{F}$, $V_{IN}>V_{OVLO}$ to $V_{OUT}=0.8 \times V_{IN}$		80		ns

Specifications subject to change without notice.

Note 1: guaranteed by characterization under room temperature.

Timing Diagrams:

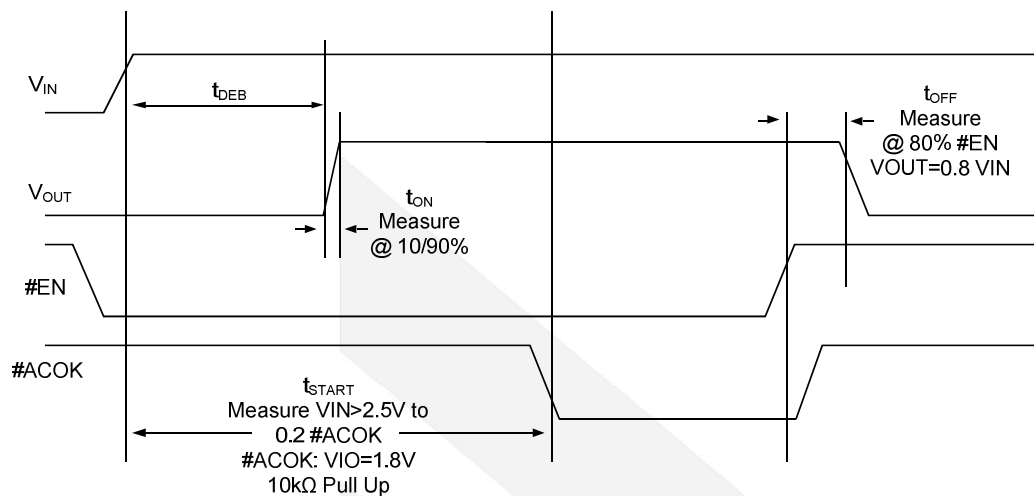


Figure 3. Timing for Power Up and Normal Operation

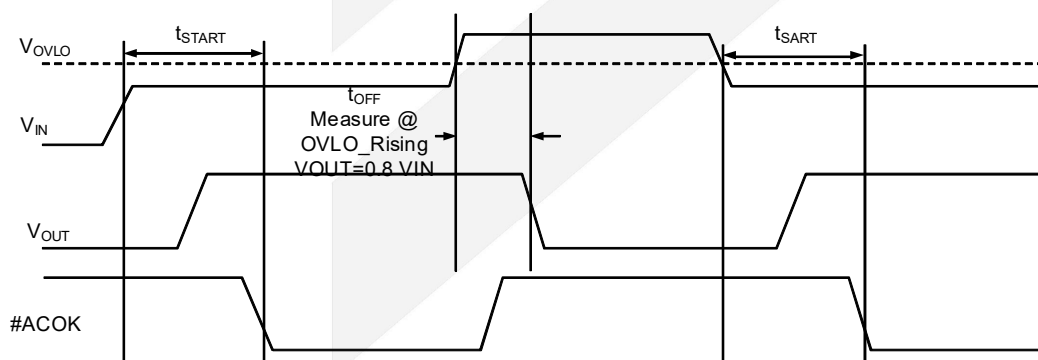
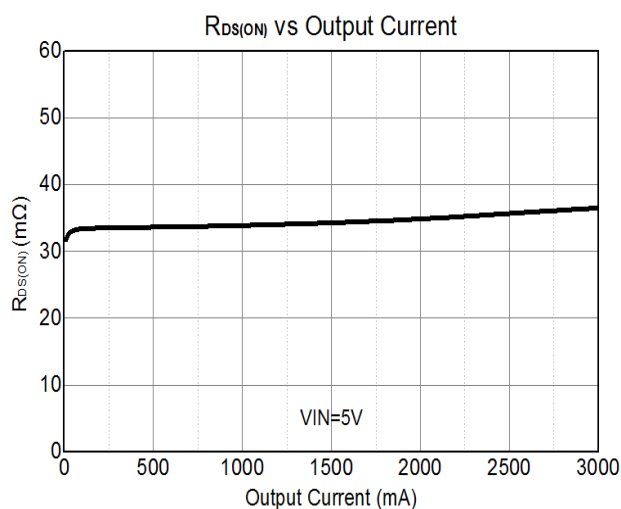
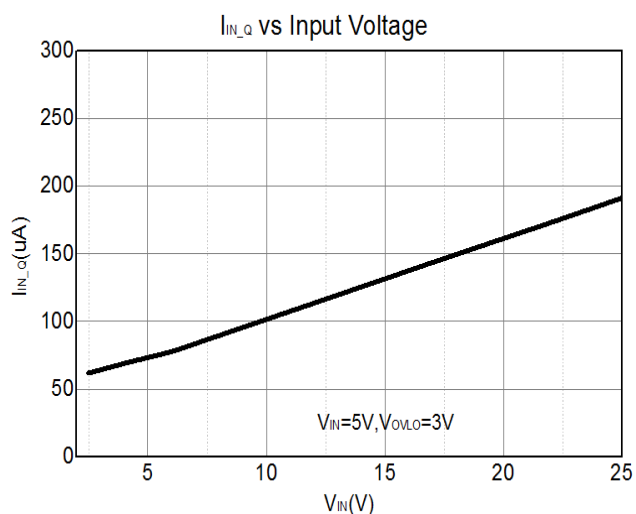


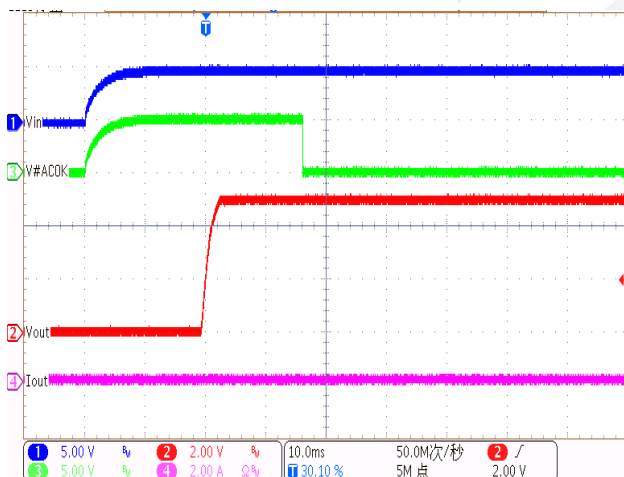
Figure 4. Timing for OVLO Trip

Typical Performance Characteristics

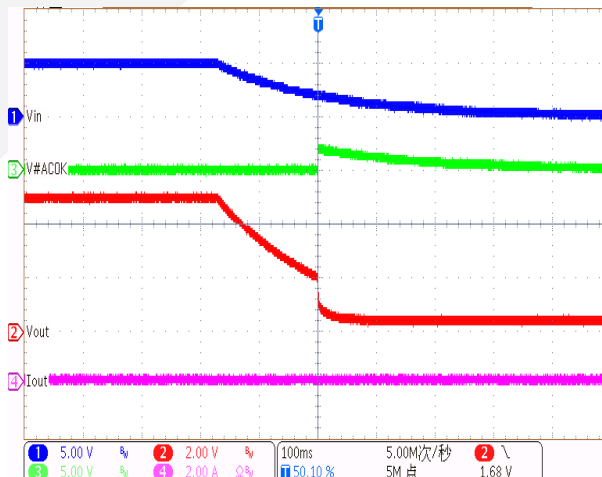
Ambient temperature is 25°C, $V_{IN}=5V$, $I_{IN}\leq 3A$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, unless otherwise noted.



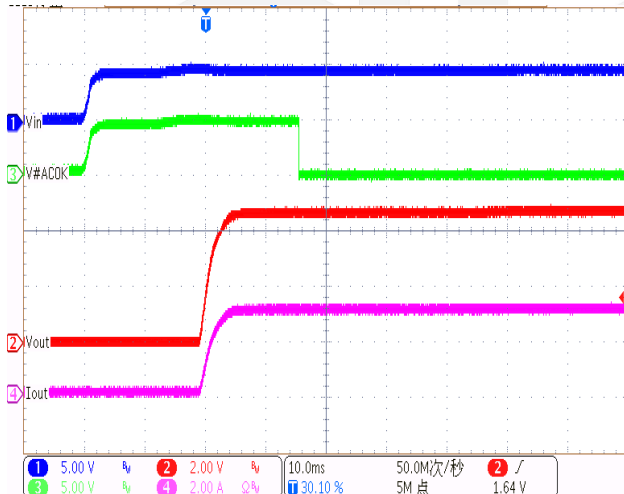
VIN Power ON ($V_{IN}=5V$, No Load)



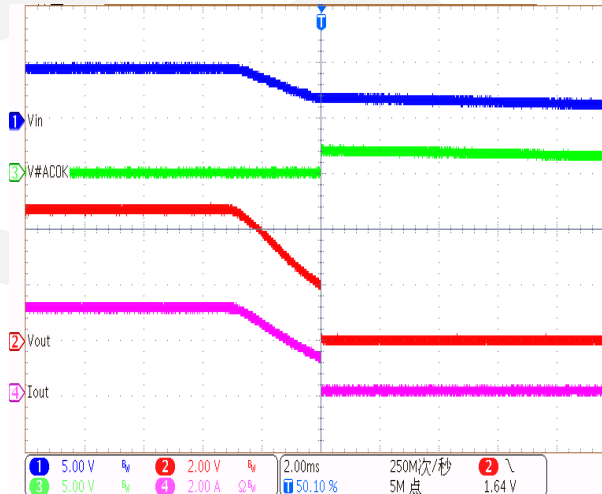
VIN Power OFF ($V_{IN}=5V$, No Load)



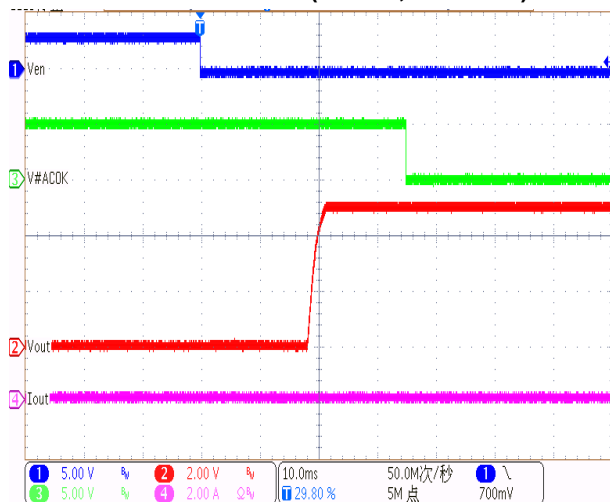
VIN Power ON ($V_{IN}=5V$, $R_{Load}=1.6ohm$)



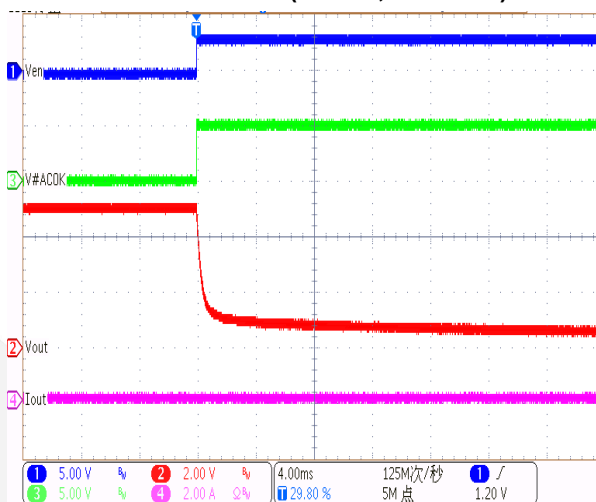
VIN Power OFF ($V_{IN}=5V$, $R_{Load}=1.6ohm$)



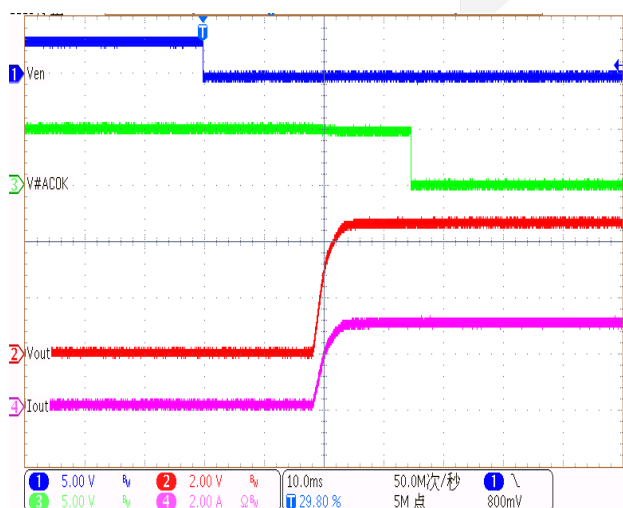
EN Power ON ($V_{IN}=5V$, No Load)



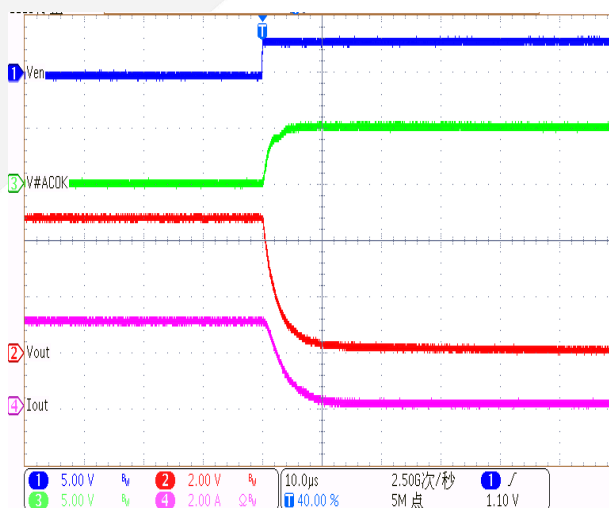
EN Power OFF ($V_{IN}=5V$, No Load)



EN Power ON ($V_{IN}=5V$, $R_{Load}=1.6\Omega$)



EN Power OFF ($V_{IN}=5V$, $R_{Load}=1.6\Omega$)





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Over-Voltage Lockout (OVLO) Calculation:

OVLO can be set externally and override default OVP. By connecting an external resistor-driver to the OVLO pin. Equation (1) can produce the desired trip voltage and resistor values.

$$V_{IN_OVLO} = V_{OVLO_TH} \times [1 + R1 / R2] \quad (1)$$

Recommended minimum R1=820kΩ.

On-The-Go (OTG) Functionality:

During OTG operation, the DIO1266 is initially disabled and the power FET's bulk diode is forward biased. The bulk diode represents ~0.7V drop across the device, which remains until the V_IN voltage increases past 2.5V, when the device is fully enabled. While the device is disabled and the body diode is forward biased, the max DC current through the diode is 1.8A. This current is limited by the thermal performance of the device (0.7V x 1.8A=1.36W). The #EN pin must be pulled LOW to ensure the device fully enables and the transient should not exceed the RC time constant of the C_IN and C_OUT capacitors. At the system level, over-voltage and current protection should be provided outside the DIO1266.





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