

DIO3581/1A_3582_3584

1.4 MHz, 60 μ A, Rail-to-Rail I/O CMOS Amplifier

Features

- Rail-to-rail input and output
 - Max offset (V_{OS}): ± 3 mV
 - Unity gain stable
 - Gain bandwidth product: 1.4 MHz
 - Very low input bias currents: 5 pA
 - Wide supply range: 2 V to 5.5 V
 - Input voltage range:
-0.1 V to +5.6 V with $V^+ = 5.5$ V
 - Ultra low power: 60 μ A per channel
 - Compact package best for portable applications
- DIO3581/1A: SOT23-5 and SC70-5
DIO3582: SOIC-8, MSOP-8 and DFN2*2-8
DIO3584: SOIC-14 and TSSOP-14

Applications

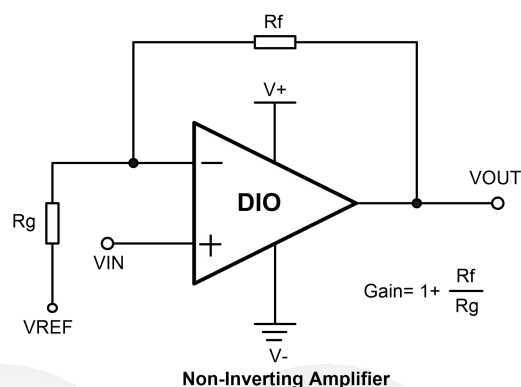
- ASIC input or output amplifiers
- Sensor interface
- Piezo electric transducer amplifiers
- Medical instrumentation
- Audio output
- Portable systems
- Smoke detectors
- Notebook PCs
- Battery-powered equipment

Descriptions

The DIO3581/1A (single), DIO3582 (dual) and DIO3584 (quad) are rail-to-rail CMOS operational amplifiers with an ultra-low offset. Features include wide input common-mode voltage range and broad output voltage swing with operating supply voltage from 2 V to 5.5 V. Products are fully specified over the extended -40 to 125°C temperature range.

The DIO3581/1A_3582_3584 provide 1.4 MHz bandwidth consuming an ultra-low current of 60 μ A per channel. Very low input bias currents of 5 pA make them ideal for integrators, photodiode amplifiers, and piezoelectric sensors.

Typical Application



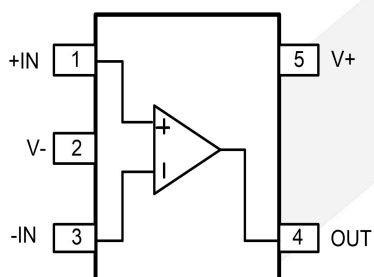
Ordering Information

Order Part Number	Top Marking		T _A	Package	
DIO3581ST5	YWXS	RoHS or Green	-40 to 125°C	SOT23-5	Tape & Reel, 3000
DIO3581SC5	YWXS	RoHS or Green	-40 to 125°C	SC70-5	Tape & Reel, 3000
DIO3581AST5	YW1A	RoHS or Green	-40 to 125°C	SOT23-5	Tape & Reel, 3000
DIO3581ASC5	YW1A	RoHS or Green	-40 to 125°C	SC70-5	Tape & Reel, 3000
DIO3582MP8	DIO358	RoHS or Green	-40 to 125°C	MSOP-8	Tape & Reel, 3000
DIO3582SO8	DIO358	RoHS or Green	-40 to 125°C	SOIC-8	Tape & Reel, 2500
DIO3582CN8	3582	RoHS or Green	-40 to 125°C	DFN2*2-8	Tape & Reel, 3000
DIO3584TP14	DIO3584	RoHS or Green	-40 to 125°C	TSSOP-14	Tape & Reel, 2500
DIO3584SO14	DIO3584	RoHS or Green	-40 to 125°C	SOIC-14	Tape & Reel, 2500

Pin Assignments

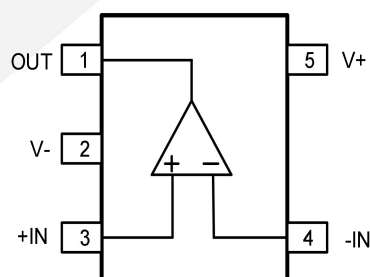
SOT23-5/SC70-5

DIO3581



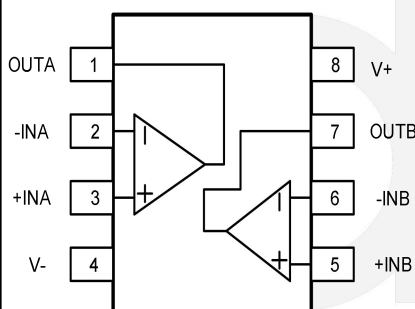
SOT23-5/SC70-5

DIO3581A



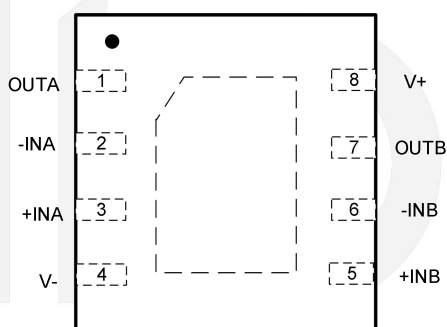
SOIC-8/MSOP-8

DIO3582



DFN2*2-8

DIO3582



TSSOP-14/SOIC-14

DIO3584

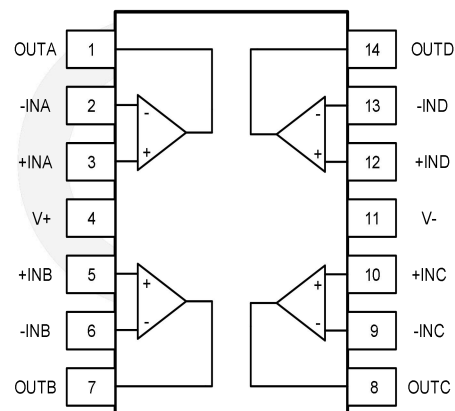


Figure 1. Pin Assignment

Pin Description

Pin name	Description
V+	Positive supply
V-	Negative supply
+INX	Positive input
-INX	Negative input
OUTX	Output

Absolute Maximum Ratings

Stresses beyond those listed under the Absolute Maximum Rating table may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Rating	Unit
V _{CC}	Supply voltage	7	V
V _{IN}	Input voltage	(V-) - 0.5 to 7	V
T _{STG}	Storage temperature range	-65 to 150	°C
T _J	Junction temperature	150	°C
T _L	Lead temperature range	260	°C
ESD	HBM, JEDEC: JESD22-A114	8	kV
	CDM, JEDEC: JESD22-C101	2	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating	Unit
V _{CC}	Supply voltage	2.0 to 5.5	V
V _{IN}	Input voltage	-0.1 to (V+) + 0.1	V
T _A	Operating temperature range	-40 to 125	°C

Electrical Characteristics

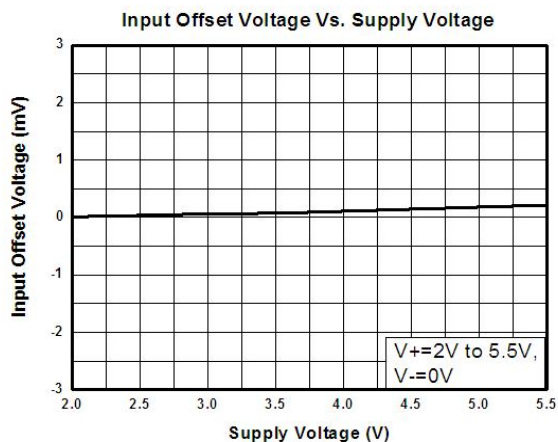
Typical value: $V_+ = 5\text{ V}$, $R_L = 100\text{ k}\Omega$ to $V_+/2$, $T_A = 25^\circ\text{C}$, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Input characteristics						
V _{OS}	Input offset voltage	-40°C ≤ T _A ≤ 125°C, V ₊ = 2 V to 5 V	-3	0.5	3	mV
I _B	Input bias current	-40°C ≤ T _A ≤ 125°C, V ₊ = 2 V to 5 V		5		pA
I _{OS}	Input offset current	-40°C ≤ T _A ≤ 125°C, V ₊ = 2 V to 5 V		5		pA
V _{CM}	Common mode voltage range		-0.1		(V ₊) +0.1	V
CMRR	Common mode rejection ratio	-40°C ≤ T _A ≤ 125°C, V _{CM} = -0.1 to (V ₊) + 0.1	65	75		dB
A _{OL}	Open loop voltage gain	R _L = 5 kΩ, V _O = 0.1 to (V ₊) - 0.1	80	100		dB
		R _L = 200 kΩ, V _O = 0.1 to (V ₊) - 0.1	90	110		
ΔV _{OS} /ΔT	Input offset voltage drift	-40°C ≤ T _A ≤ 125°C			5	μV/°C
Output characteristics						
V _{OH}	Output voltage high	R _L = 100 kΩ, -40°C ≤ T _A ≤ 125°C		4.998		V
V _{OL}	Output voltage low	R _L = 100 kΩ, -40°C ≤ T _A ≤ 125°C		2.5		mV
I _{SC}	Output short circuit current	Source I _{SC} , V ₊ = 5 V		40		mA
		Sink I _{SC} , V ₊ = 5 V		40		
Power supply						
PSRR	Power supply rejection ration		65	85		dB
I _S	Supply current per channel/Amp	-40°C ≤ T _A ≤ 125°C		60		μA
Dynamic performance						
GBP	Gain bandwidth product	R _L = 100 K, C _L = 100 pF		1.4		MHz
SR	Slew rate	G = 1, 2 V output step		0.6		V/μs
t _s	Setting time	G = 1, 2 V output step		2.4		μs
Noise performance						
THD	Total harmonic distortion	f = 10 kHz, 2 V output step, R _L = 10 kΩ		0.019		%
e _n	Voltage noise density	f =1 kHz		45		nV/√Hz
		f = 10 kHz		35		

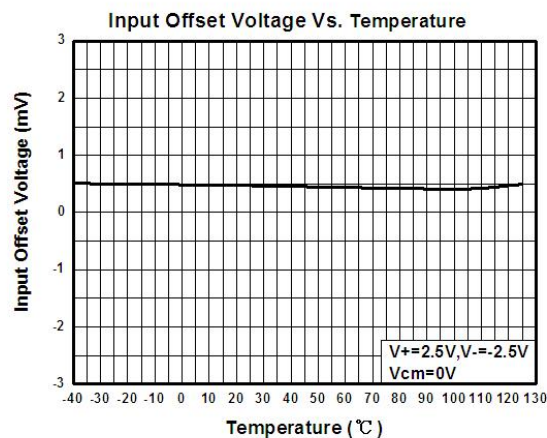
Specifications subject to change without notice.

Typical Performance Characteristics

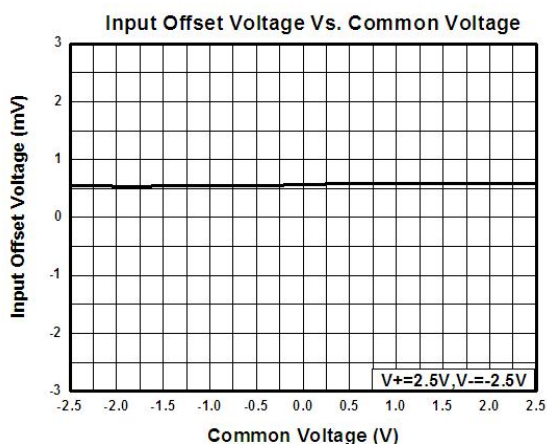
All typical value are at $V_+ = 2.5\text{ V}$, $V_- = -2.5\text{ V}$, $T_A = 25^\circ\text{C}$, unless otherwise specified.



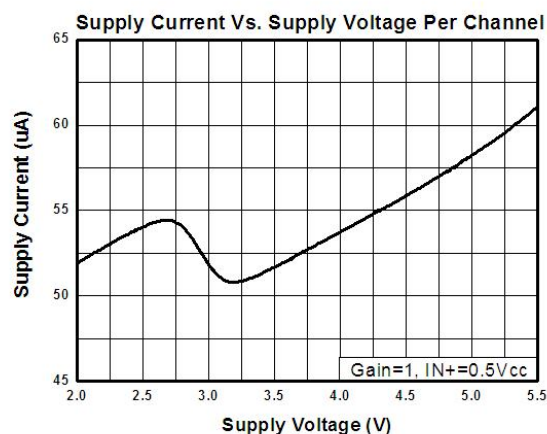
Input offset voltage vs. Supply voltage



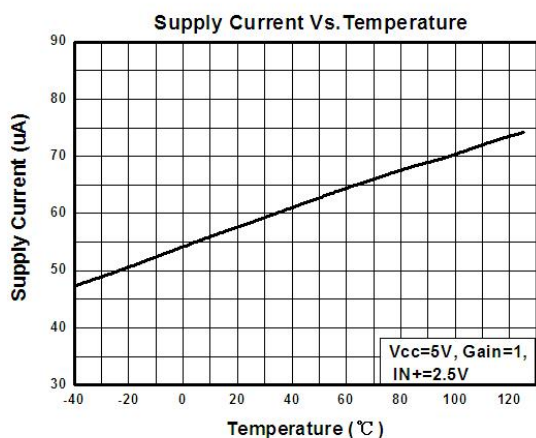
Input offset voltage vs. Temperature



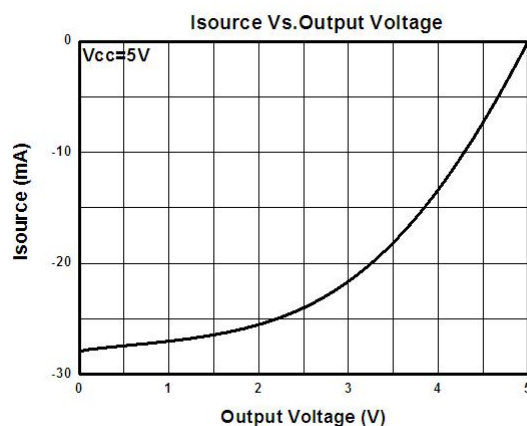
Input offset voltage vs. Common voltage



Supply current vs. Supply voltage per channel



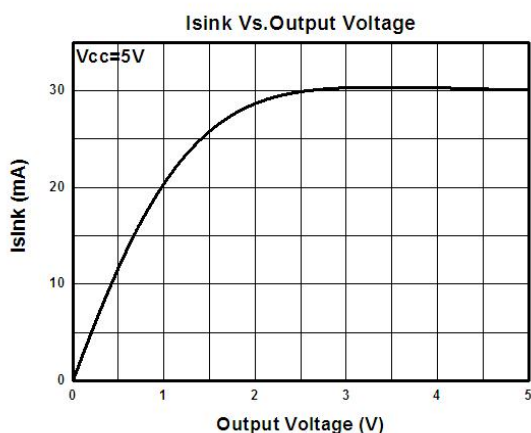
Supply current vs. Temperature



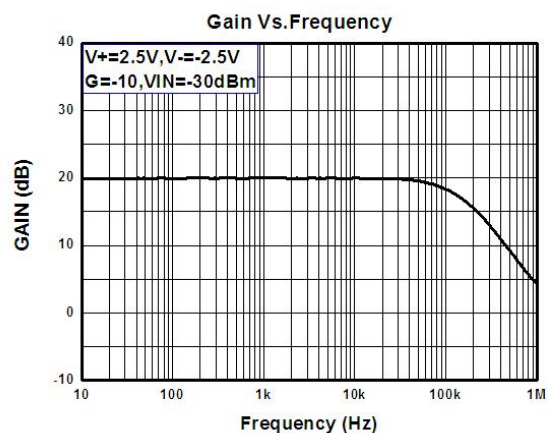
ISOURCE vs. Output voltage

Typical Performance Characteristics(continued)

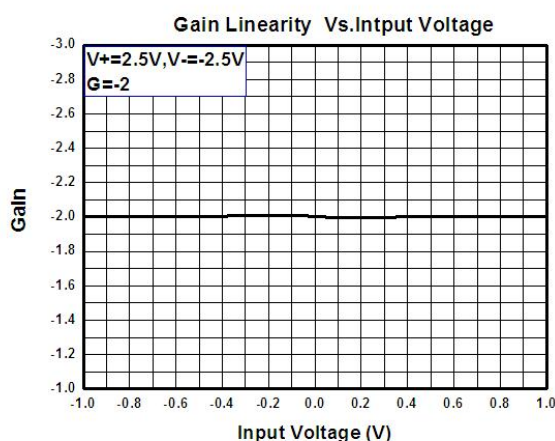
All typical value are at $V_+ = 2.5$ V, $V_- = -2.5$ V, $T_A = 25^\circ\text{C}$, unless otherwise specified.



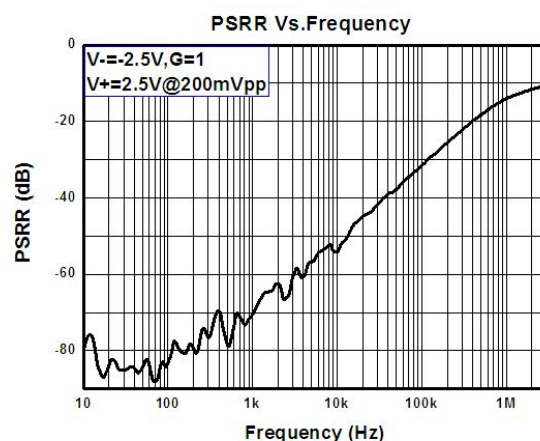
Isink vs. Output voltage



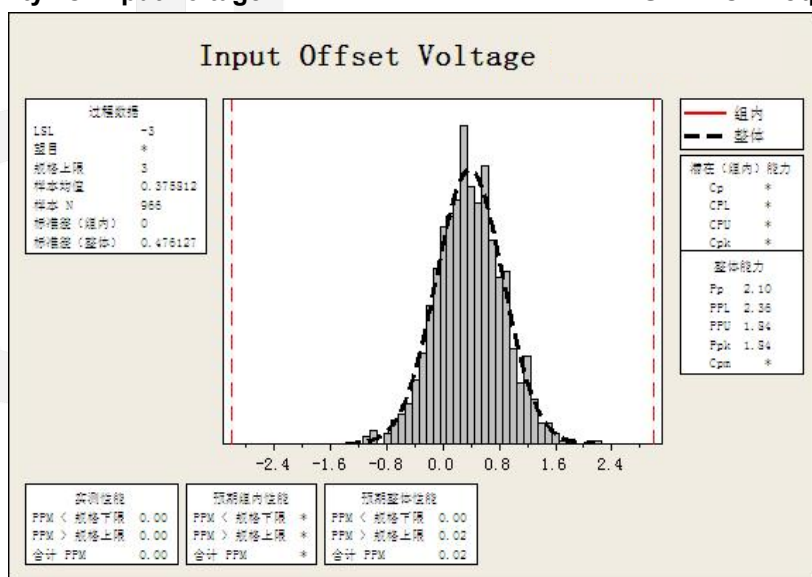
Gain vs. Frequency



Gain linearity vs. Input voltage



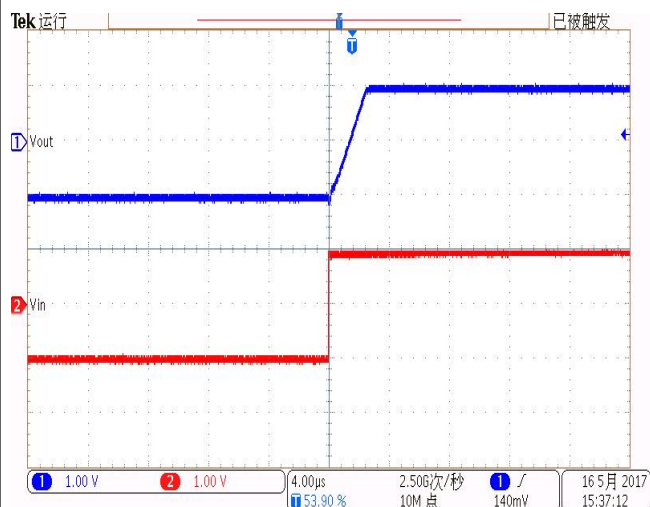
PSRR vs. Frequency



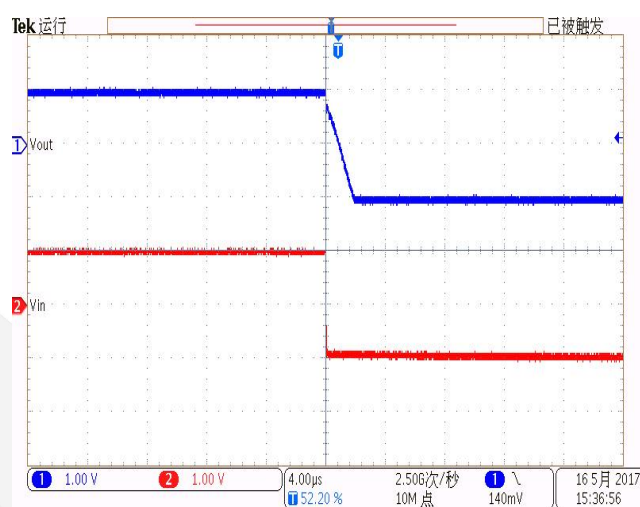
Input offset voltage statistics

Typical Performance Characteristics(continued)

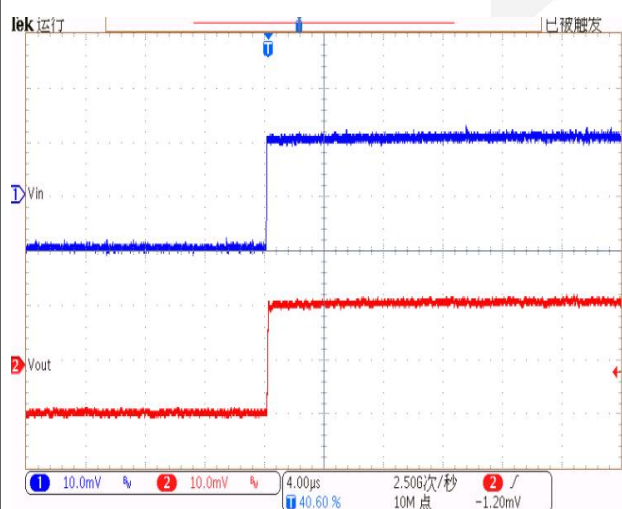
All typical value are at $V_+ = 2.5$ V, $V_- = -2.5$ V, $G = 1$, $R_L = 1$ M Ω , $T_A = 25^\circ\text{C}$, unless otherwise specified.



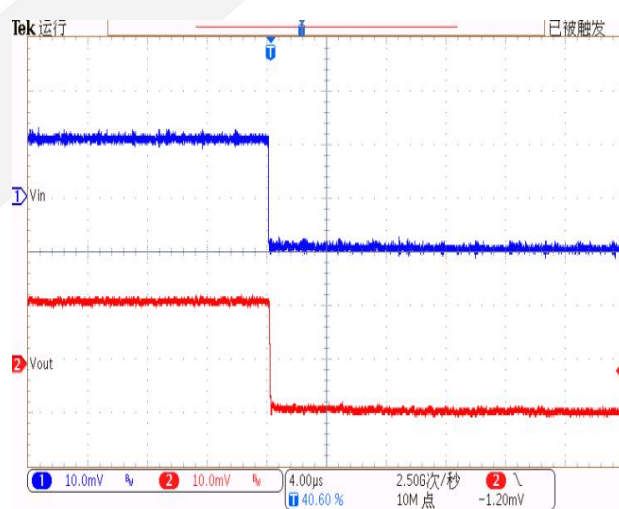
Slew rate (+)



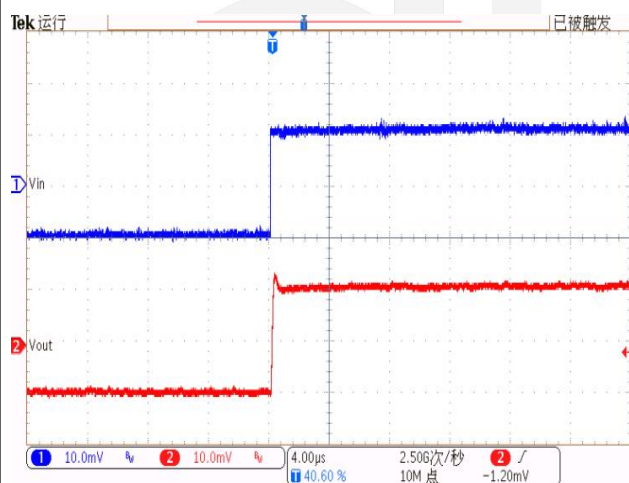
Slew rate (-)



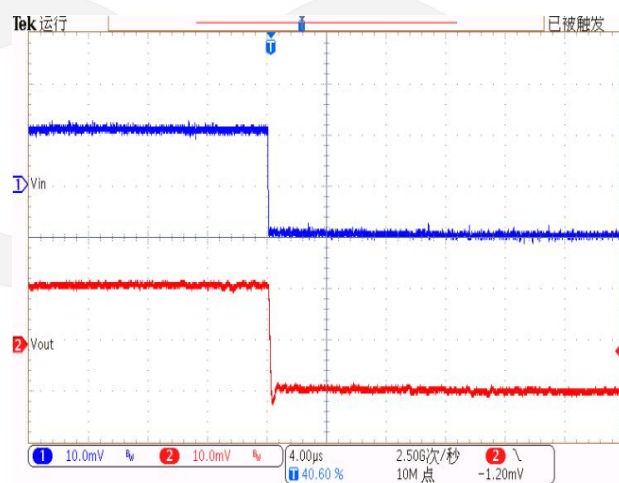
Small-signal response rise ($C_L = \text{No load}$)



Small-signal response fall ($C_L = \text{No load}$)



Small-signal response rise ($C_L = 200$ pF)



Small-signal response fall ($C_L = 200$ pF)

CONTACT US

Dioo is a professional design and sales corporation for high-quality and performance analog semiconductors. The company focuses on industry markets, such as cell phones, handheld products, laptops, medical equipment, and so on. Dioo's product families include analog signal processing and amplifying, LED drivers, and charger ICs. Go to <http://www.dioo.com> for a complete list of Dioo product families.

For additional product information or full datasheet, please contact our sales department or representatives.