

Low-Power, USB 2.0 High-Speed Switch

Features

- Super high bandwidth (-3 dB): 2.0 GHz
- VCC operation at 2.7 V to 5.5 V
- Low C_{ON} : 5 pF (typ.)
- Low R_{ON} : 5.5 Ω (typ.)
- Low power consumption: 1 μ A (max.)
- Low I_{CCT} : 0.5 μ A (typ.) at $V_{CNTRL} = 1.8$ V, $V_{CC} = 3.6$ V
- ± 6 kV HBM ESD on all pins
- Power-off/on protection on common ports

Applications

- Telematics control units
- Automotive cluster displays
- Front and rear cameras

Package Information

Part Number	Package	Body Size
DIO32730	QFN10	1.8 mm × 1.4 mm

Description

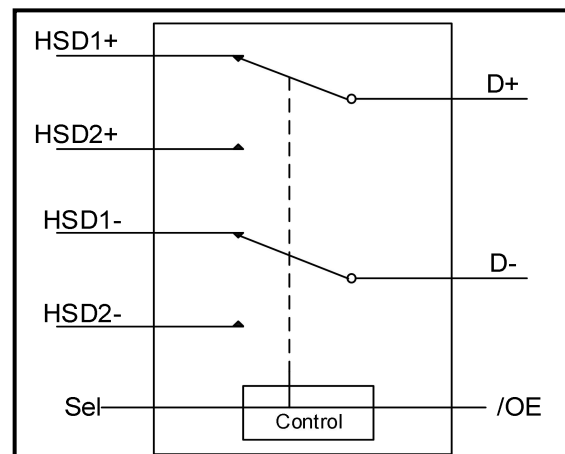
The DIO32730 is a low-power, dual SPDT 2-port high-speed analog switch. It handles bidirectional signal flow optimized for switching a high-speed (480 Mbps) source or a full-speed (12 Mbps) source.

The DIO32730 has a high channel-to-channel noise isolation and a low bit-to-bit skew which allows it to pass high-speed differential signals with good signal integrity. Each switch offers little or no attenuation of the high-speed signals at the outputs.

The DIO32730 contains special circuitry on the D+/D- pins, which can tolerate up to 5.5 V when the USB devices are either powered off or powered on.

The DIO32730 is available in the Green package: QFN1.8×1.4-10.

Simplified Schematic



■ Ordering Information

Ordering Part No.	Top Marking	MSL	RoHS	T _A	Package	
DIO32730LP10	YW3V	1	Green	-40 to 85°C	DQFN1.8×1.4-10	Tape & Reel, 3000

If you encounter any issue in the process of using the device, please contact our customer service at marketing@dioo.com or phone us at (+86)-21-62116882. If you have any improvement suggestions regarding the datasheet, we encourage you to contact our technical writing team at docs@dioo.com. Your feedback is invaluable for us to provide a better user experience.

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1. Pin Assignment and Functions

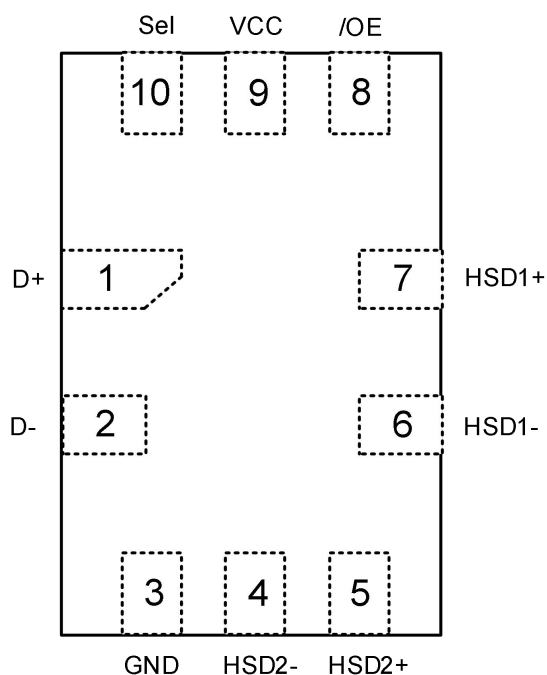


Figure 1. QFN1.4×1.8-10 (Top view)

Table 1. Pin descriptions

Pin Name	Description
/OE	Switch enable
Sel	Switch select
D+, D-	USB data bus
HSDn+, HSDn-	Multiplexed source inputs
VCC	Power supply
GND	Ground

Table 2. Truth table

Sel	/OE	Function
X	H	Disconnect
H	L	D+, D- = HSD1+, HSD1-
L	L	D+, D- = HSD2+, HSD2-

2. Absolute Maximum Ratings

Exceeding the maximum ratings listed under Absolute Maximum Ratings when designing is likely to damage the device permanently. Do not design to the maximum limits because long-time exposure to them might impact the device's reliability. The ratings are obtained over an operating free-air temperature range unless otherwise specified.

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply voltage	-0.3	6.0	V
V_{CNTRL}	DC input voltage (S, /OE)	-0.3	V_{CC}	V
V_{SW}	DC input I/O voltage	-0.3	$V_{CC} + 0.3$	V
	DC input I/O voltage (D+/D-)	-0.3	$V_{CC} + 0.3$	V
$V_{D+/D-}$	D+/D- DC voltage ⁽¹⁾	-0.3	9	V
I_{IK}	DC input diode current	-50		mA
I_{OUT}	DC output current		50	mA
T_{STG}	Storage temperature	-65	150	°C

Note:

(1) This rating only applies to the D+/D- pin with respect to GND. V_{CC} must be powered within the recommended operating conditions of 2.7 V to 5.5 V and the /OE pin must be logic high for this rating to be applicable. Any condition where V_{CC} is unpowered or the /OE pin is not high must refer to the rest of the Absolute Maximum Ratings Table.

3. Recommended Operating Conditions

Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. The ratings are obtained over an operating free-air temperature range unless otherwise specified.

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply voltage	2.7	5.5	V
V_{CNTRL}	Control input voltage (S, /OE)	0	V_{CC}	V
V_{SW}	Switch I/O voltage (HSD1±, HSD2±)	0	V_{CC}	V
	Switch I/O voltage (D+, D-)	0	V_{CC}	V
T_A	Operating temperature	-40	85	°C

4. ESD Ratings

When a statically-charged person or object touches an electrostatic discharge sensitive device, the electrostatic charge might be drained through sensitive circuitry in the device. If the electrostatic discharge possesses sufficient energy, damage might occur to the device due to localized overheating.

Model	Condition	Value	Unit
HBM	JEDEC: JESD22-A114 all pins	±6	kV

5. DC Electrical Characteristics

All typical value are at $T_A = 25^\circ\text{C}$, unless otherwise specified.

Symbol	Parameter	Conditions	V_{CC} (V)	Min	Typ	Max	Unit
V_{IH}	Input voltage high		3.0 to 3.6	0.85			V
V_{IL}	Input voltage low		3.0 to 3.6			0.35	V
I_{IN}	Control input leakage	$V_{SW} = 0$ to V_{CC}	3.6	-1		1	μA
I_{OZ}	Off-state leakage	$0 \leq D_n, HSD1n, HSD2n \leq 3.6$ V	3.6	-1		1	μA
I_{OFF}	Power-off leakage current (all common ports)	$V_{SW} = 0$ V to 3.6 V, $V_{CC} = 0$ V, See Figure 8	0	-1		1	μA
R_{ON}	HS switch on resistance	$V_{SW} = 0.4$ V, $I_{ON} = 8$ mA, See Figure 7	3.3		5.5	8	Ω
ΔR_{ON}	HS delta R_{ON}	$V_{SW} = 0.4$ V, $I_{ON} = 8$ mA	3.3		0.1		Ω
I_{CC}	Quiescent supply current	$V_{CNTRL} = 0$ or V_{CC}	3.6		0.4	1	μA
I_{CCT}	Increase in I_{CC} current per control voltage and V_{CC}	$V_{CNTRL} = 2.6$ V, $V_{CC} = 3.6$ V	3.6		0.5	1	μA
		$V_{CNTRL} = 1.8$ V, $V_{CC} = 3.6$ V	3.6		0.5	1	μA

Note:

(1) Specifications subject to change without notice.

6. AC Electrical Characteristics

All typical value are for $V_{CC} = 3.3\text{ V}$ at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V_{CC} (V)	Temp	Min	Typ	Max	Unit
t_{ON}	Turn-on time S, /OE to output	$R_L = 50\ \Omega$, $C_L = 5\text{ pF}$, $V_{SW} = 0.8\text{ V}$, See Figure 14	3.0 to 3.6	full			3.0	us
t_{OFF}	Turn-off time S, /OE to output	$R_L = 50\ \Omega$, $C_L = 5\text{ pF}$, $V_{SW} = 0.8\text{ V}$, See Figure 14	3.0 to 3.6	full			14	ns
t_{PD}	Propagation delay	$R_L = 50\ \Omega$, $C_L = 5\text{ pF}$	3.3	25°C		0.25		ns
				full			5.0	ns
t_{BBM}	Break-before-make	$R_L = 50\ \Omega$, $C_L = 5\text{ pF}$, $V_{SW} = 0.8\text{ V}$, See Figure 13	3.0 to 3.6	25°C		12		ns
				full	10		28	ns
O_{IRR}	Off- isolation	$R_L = 50\ \Omega$, $f = 240\text{ MHz}$, See Figure 12	3.0 to 3.6	25°C		-37		dB
X_{TALK}	Non-adjacent channel crosstalk	$R_L = 50\ \Omega$, $f = 240\text{ MHz}$, See Figure 11	3.0 to 3.6	25°C		-45		dB
BW	-3dB bandwidth	$R_L = 50\ \Omega$, $C_L = 0\text{ pF}$, See Figure 10	3.0 to 3.6	25°C		2000		MHz
		$R_L = 50\ \Omega$, $C_L = 5\text{ pF}$, See Figure 10		25°C		720		MHz
$t_{SK(P)}$	Skew of opposite transitions of the same output	$R_L = 50\ \Omega$, $C_L = 5\text{ pF}$	3.0 to 3.6	25°C		20		ps

Note:

(1) Specifications subject to change without notice.

7. Capacitance

Symbol	Parameter	Conditions	Temp	Min	Typ	Max	Unit
C_{IN}	Control pin input capacitance	$V_{CC} = 0\text{ V}$	25°C		1.2		pF
C_{ON}	D+/D- on capacitance	$V_{CC} = 3.3\text{ V}$, /OE = 0 V, $f = 240\text{ MHz}$, See Figure 9	25°C		5		
C_{OFF}	HSD1n, HSD2n off capacitance	V_{CC} and /OE = 3.3 V, See Figure 9	25°C		2		

8. Typical Characteristics

$T_A = 25^\circ\text{C}$, $V_{CC} = 3.6\text{ V}$, unless otherwise specified.

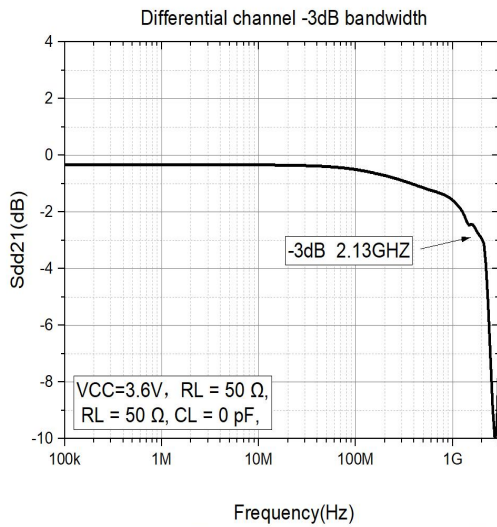


Figure 2. -3dB bandwidth vs. Frequency

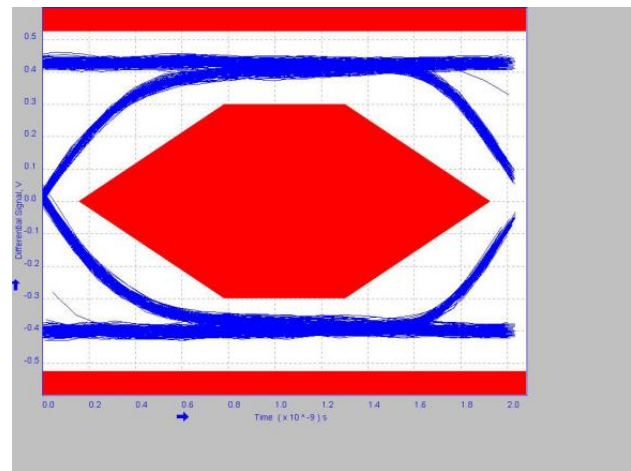


Figure 3. 480 Mbps with USB switch in signal path

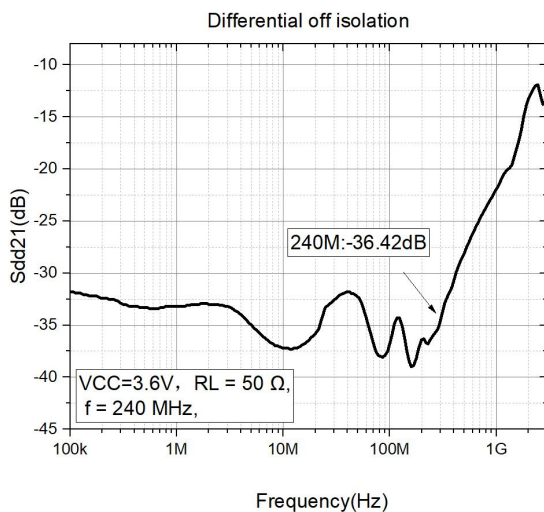


Figure 4. Off-isolation vs. Frequency

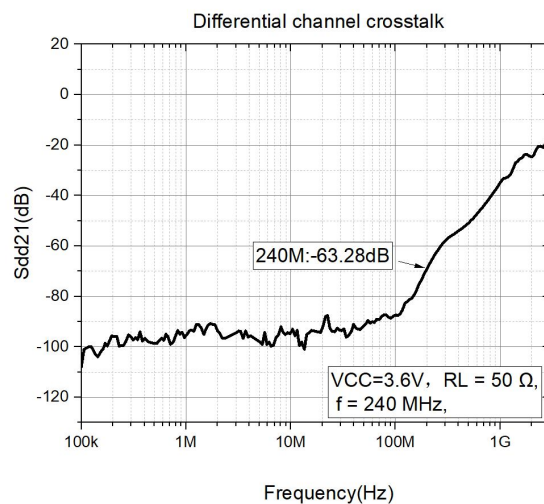


Figure 5. Crosstalk vs. Frequency

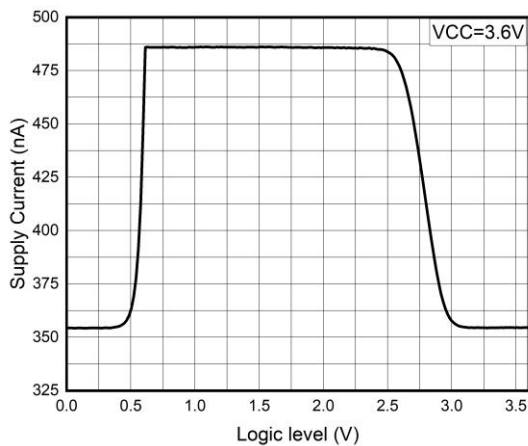


Figure 6. Supply current vs. Logic level

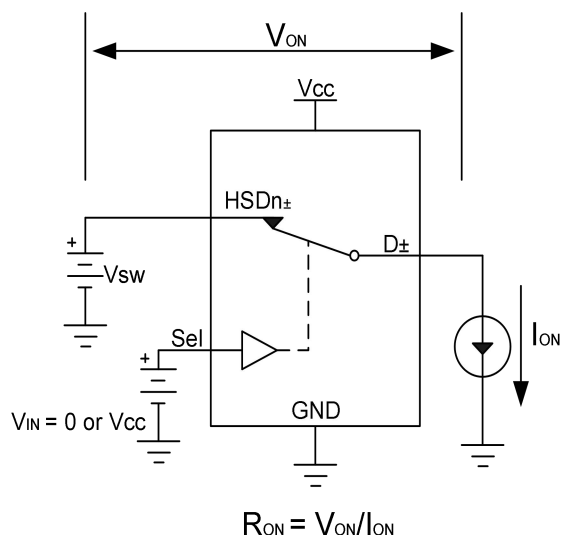


Figure 7. Switch on resistor

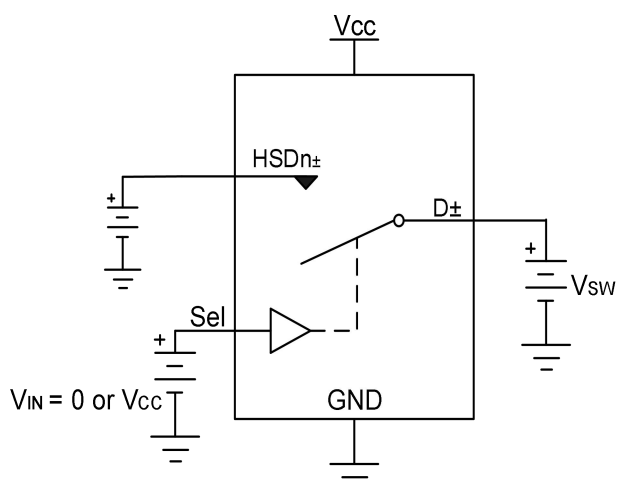


Figure 8. Switch off leakage

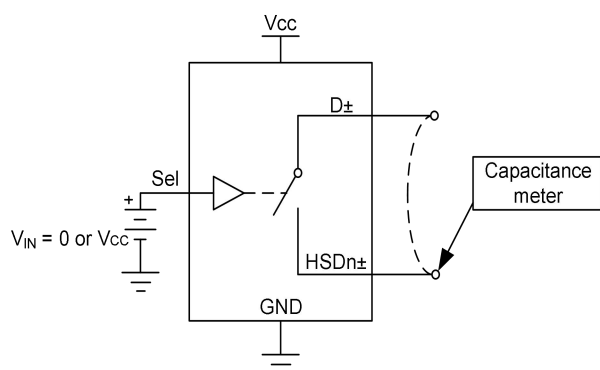


Figure 9. Channel on/off capacitance

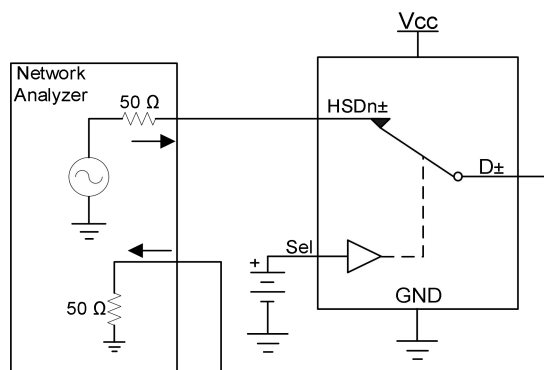


Figure 10. Bandwidth

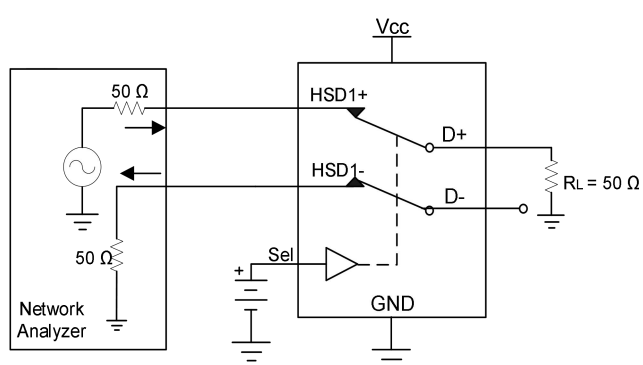


Figure 11. Channel-to-channel crosstalk

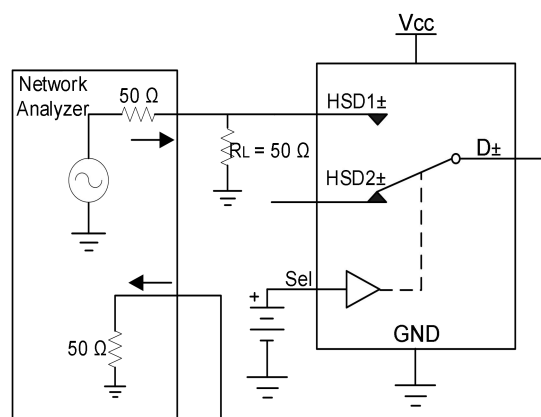


Figure 12. Off-isolation

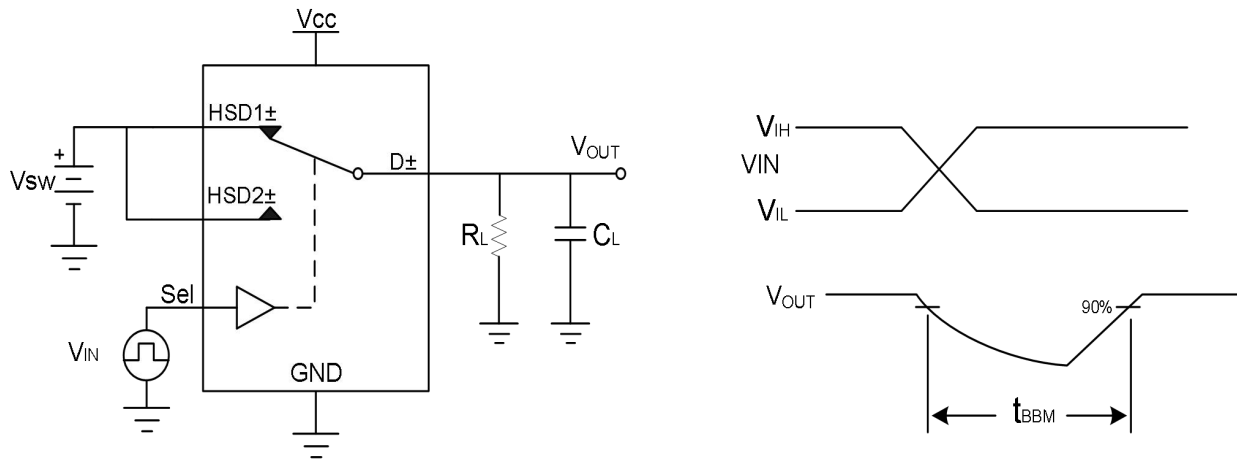


Figure 13. Break-before-make

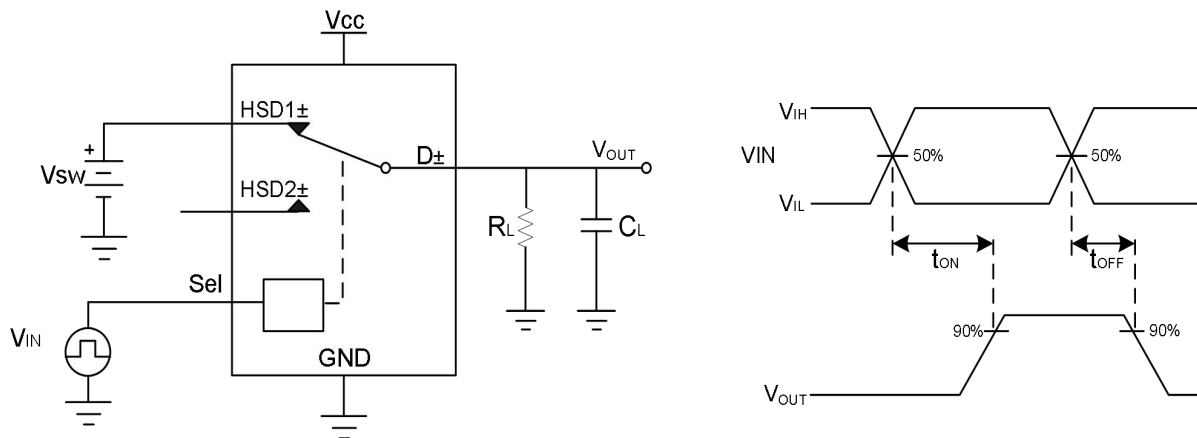
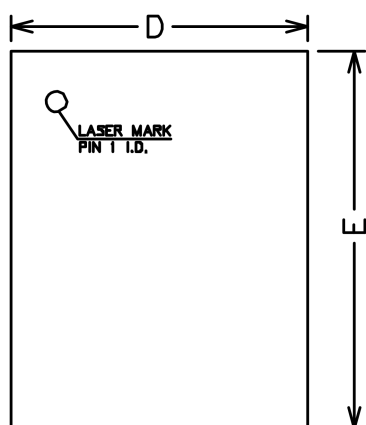
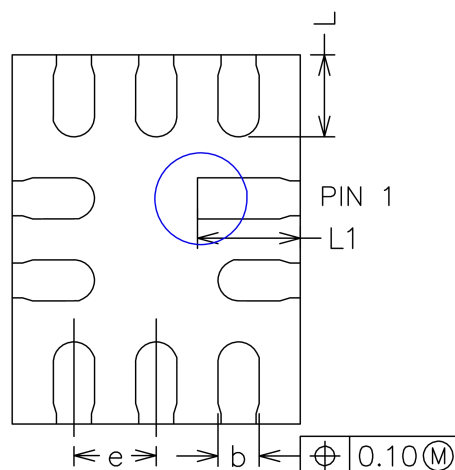


Figure 14. Turn-on/Turn-off

9. Physical Dimensions: QFN1.8×1.4-10



TOP VIEW

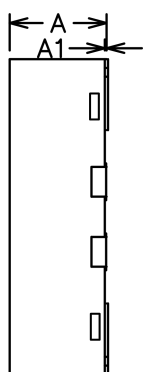
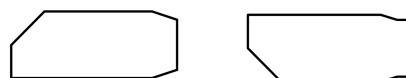


BOTTOM VIEW



SIDE VIEW

Two options



SIDE VIEW

Common Dimensions (Units of Measure = Millimeter)			
Symbol	Min	Nom	Max
A	0.50	0.55	0.60
A1	0.00	0.02	0.05
b	0.15	0.20	0.25
D	1.30	1.40	1.50
E	1.70	1.80	1.90
e	0.30	0.40	0.50
L	0.35	0.40	0.45
L1	0.45	0.50	0.55

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