

DIO6199

Synchronous Boost Converter with Ultra-Low Quiescent Current

Features

- Operating input voltage range: 1.1 V to 5.5 V
- Adjustable output voltage range: 2.0 V to 5.5 V
- Ultra-low quiescent current (V_{OUT} pin): 600 nA
- Ultra-low quiescent current (V_{IN} pin): 400 nA
- Switch peak current limit
- Regulated output voltage in Down mode
- True disconnection during shutdown
- Up to 93% efficiency from 10 mA to 300 mA Load
- Package: DFN2*2-6 and SOT23-6

Applications

- Portable products
- Battery powered systems
- Low power wireless applications
- Wearable applications
- Memory LCD bias
- Optical heart rate monitor LED bias

Descriptions

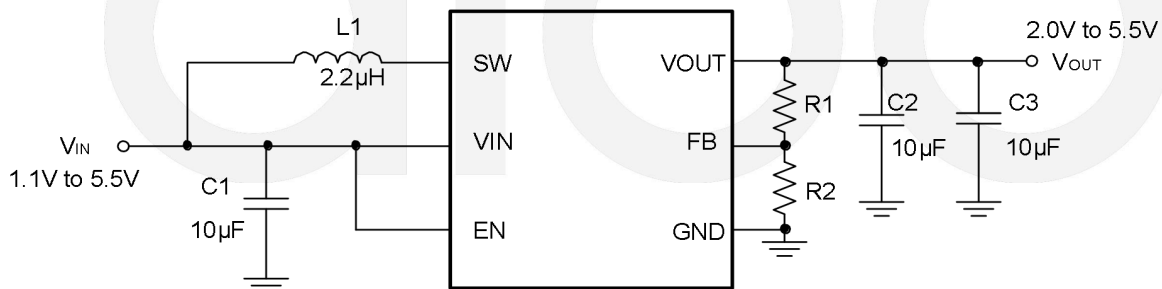
The DIO6199 is a synchronous boost converter with 1 μ A ultra-low quiescent current. It can operate efficiently under light load conditions, which is essential to prolong the service life of batteries.

The DIO6199 boost converter adopts Constant Off Time(COT) control topology, which can achieve the highest efficiency with the lowest quiescent current. The device consumes only 1 μ A quiescent current under light load.

The DIO6199 can also provide Down mode and Pass-through mode for different applications. In Down mode, even if the input voltage is higher than the output voltage, the output voltage can be adjusted as the target value. In Pass-through mode, the output voltage varies with the input voltage. When $V_{IN} > V_{OUT} + 0.5$ V, the DIO6199 exits the Down mode and transfers to Pass-through mode.

The DIO6199 can disconnect the load from the input power supply when it is disabled, thus realizing the real shutdown, thus reducing the current consumption. The device is packaged in DFN2*2-6 and SOT23-6.

Typical Application



Ordering Information

Order Part No.	Top Marking	RoHS	T _A	Package	
DIO6199DN6	DFAZ	Green	-40 to 85°C	DFN2*2-6	Tape & Reel, 3000
DIO6199ST6	WFAZ	Green	-40 to 85°C	SOT23-6	Tape & Reel, 3000

Pin Assignment

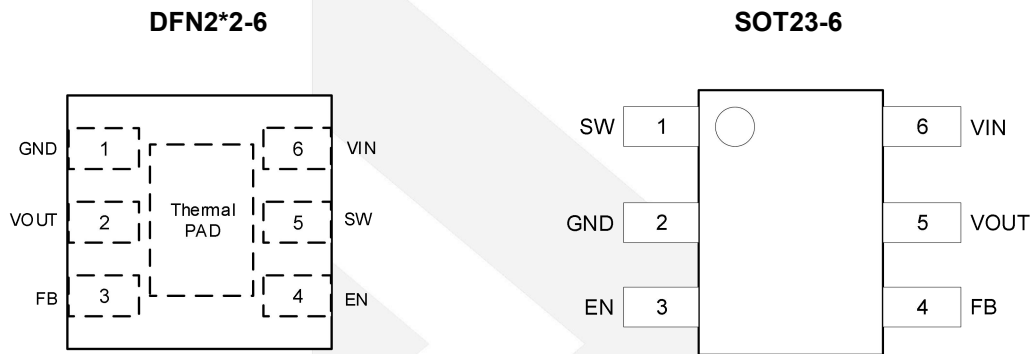


Figure 1. Top View

Pin Descriptions

Pin Name	Type	Description
VIN	I	IC power supply input.
SW	PWR	Switch pin of the converter. It is connected to the inductor.
EN	I	Enable logic input. Logic high voltage enables the device; logic low voltage disables the device. Do not leave it floating.
GND	PWR	Ground.
VOUT	PWR	Boost converter output.
FB	I	Voltage feedback of adjustable output voltage. Connect to the center tap of a resistor divider to program the output voltage.
Thermal PAD	NC	Not connected internally. Can be connected to GND.

Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Rating” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter		Rating	Unit
	Voltage range at terminals ⁽¹⁾ (VIN, SW, VOUT, FB, EN)		-0.3 to 6.0	V
T _J	Operating junction temperature		-40 to 150	°C
T _{STG}	Storage Temperature		-65 to 150	°C
R _{θJA}	Junction-to-ambient thermal resistance	SOT23-6	150	°C/W
		DFN2*2-6	71.7	°C/W
ESD	Human Body Model (HBM)		±2000	V
	Charged Device Model (CDM)		±500	V

Note:

(1) All voltage values are with respect to network ground terminal.

Electrical Characteristics

$T_J = -40^{\circ}\text{C}$ to 125°C and $V_{IN} = 1.1\text{ V}$ to 5.5 V . Typical values are at $V_{IN} = 3.7\text{ V}$, $T_J = 25^{\circ}\text{C}$, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Power Supply						
V_{IN}	Input voltage range		1.1		5.5	V
$V_{UVLO}^{(1)}$	Input under voltage lockout threshold	V_{IN} rising		0.9	1.1	V
	UVLO hysteresis			200		mV
I_Q	Quiescent current into V_{IN} pin	IC enabled, no Load, no Switching, Boost or Down Mode $T_J = -40^{\circ}\text{C}$ to 85°C		0.4	1.1	μA
	Quiescent current into V_{OUT} pin	IC enabled, no Load, no Switching, Boost or Down Mode $T_J = -40^{\circ}\text{C}$ to 85°C		0.6	1.5	μA
I_{SD}	Shutdown current into V_{IN} pin	IC disabled, $V_{IN} = 3.7\text{ V}$, $V_{OUT} = 0\text{ V}$ $T_J = -40^{\circ}\text{C}$ to 85°C		0.5	1.6	μA
Output						
V_{OUT}	Output voltage range		2.0		5.5	V
V_{REF}	Feedback reference voltage	$V_{IN} < V_{OUT}$, PWM mode	0.98	1.00	1.02	V
		$V_{IN} < V_{OUT}$, PFM mode		1.03		V
$V_{OVP}^{(1)}$	Output overvoltage protection threshold	V_{OUT} rising	5.6	5.8	6.0	V
	OVP hysteresis			200	400	mV
$I_{FB_LKG}^{(1)}$	Leakage current into FB pin	$V_{FB} = 1.0\text{ V}$		10	50	nA
Power Switch						
$R_{DS(on)_LS}$	Low side switch on resistance	$V_{OUT} = 5.0\text{ V}$		250		m Ω
		$V_{OUT} = 3.3\text{ V}$		300		m Ω
		$V_{OUT} = 2.0\text{ V}$		400		m Ω
$R_{DS(on)_HS}$	Rectifier on resistance	$V_{OUT} = 5.0\text{ V}$		300	350	m Ω
		$V_{OUT} = 3.3\text{ V}$		350	450	m Ω
		$V_{OUT} = 2.0\text{ V}$		500	750	m Ω
f_{SW}	Operation frequency			1		MHz
$I_{LIM}^{(1)}$	Current limit threshold	$V_{OUT} \geq 2.5\text{ V}$, boost operation	0.8	1.0	1.25	A
		$V_{OUT} < 2.5\text{ V}$, boost operation	0.5	0.75		A
I_{SW_LKG}	Leakage current into SW pin (from SW pin to GND)	$V_{SW} = 5.0\text{ V}$, no switch, $T_J = -40^{\circ}\text{C}$ to 85°C			200	nA
Control logic						
V_{IL}	EN input low voltage threshold	$V_{IN} \leq 1.5\text{ V}$			$0.2 \times V_{IN}$	V
$V_{IH}^{(1)}$	EN input high voltage threshold	$V_{IN} \leq 1.5\text{ V}$	$0.8 \times V_{IN}$			V
V_{IL}	EN input low voltage threshold	$V_{IN} > 1.5\text{ V}$			0.4	V
$V_{IH}^{(1)}$	EN input high voltage threshold	$V_{IN} > 1.5\text{ V}$	1.2			V
$I_{EN_LKG}^{(1)}$	Leakage current into EN pin	$V_{EN} = 5.0\text{ V}$			50	nA



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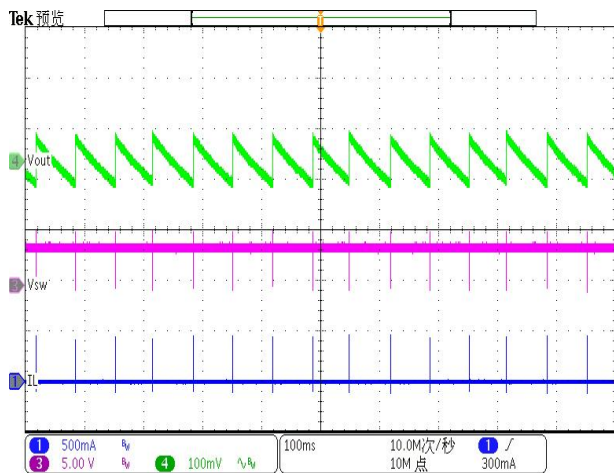
	Over temperature protection			150		°C
	Over temperature hysteresis			25		°C

Note:

- (1) Guaranteed by design.
- (2) Specifications subject to change without notice.

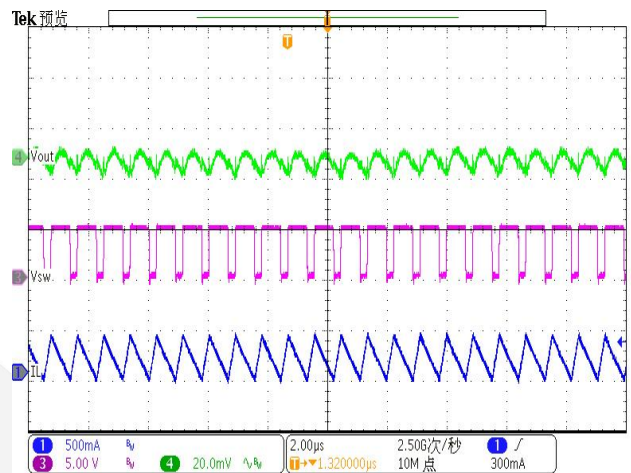


Typical Characteristic



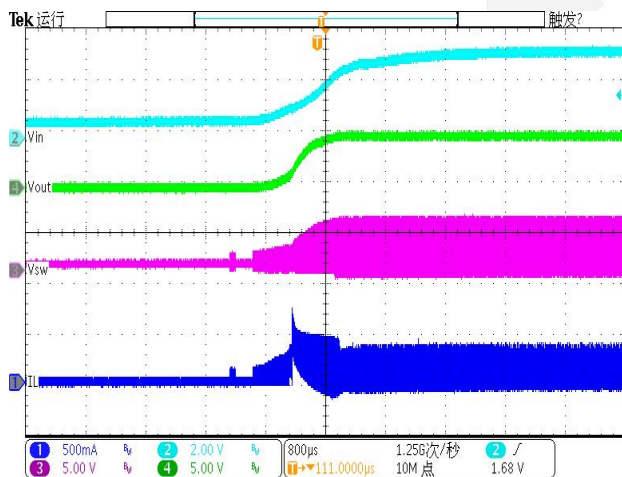
$V_{IN} = 3.7\text{ V}$, No Load at Light Load

Figure 2. Switching Waveform at Light Load



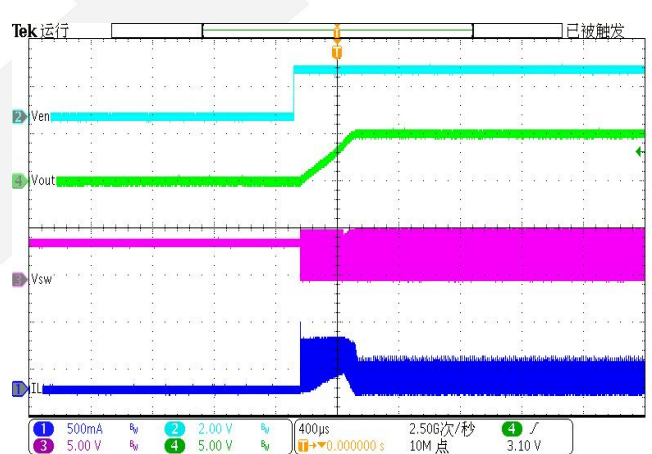
$V_{IN} = 3.7\text{ V}$, Load = 100 mA at Heavy Load

Figure 3. Switching Waveform at Heavy Load



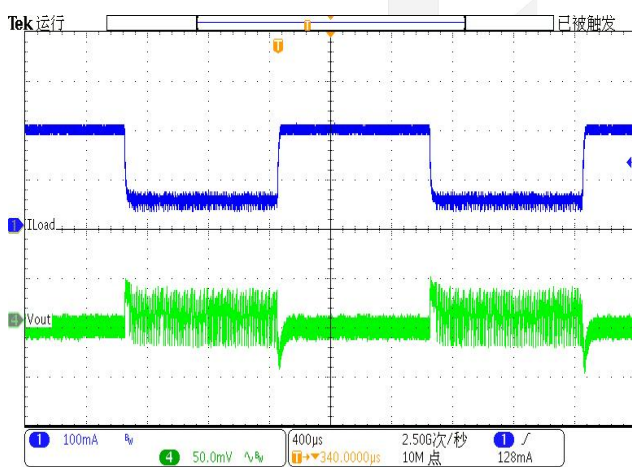
$V_{IN} = 3.7\text{ V}$, Load = 50 mA

Figure 4. Startup by VIN



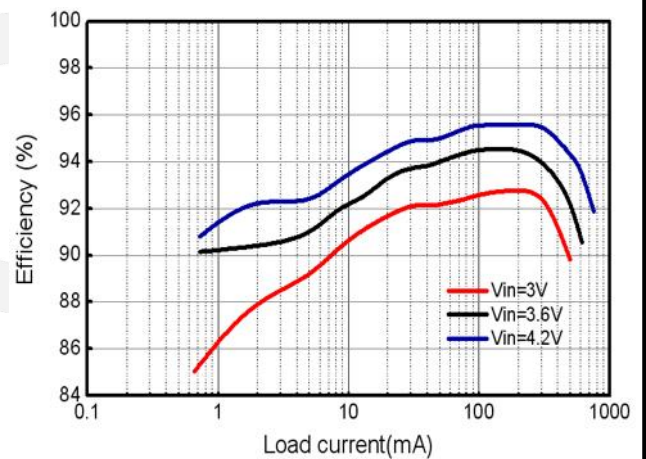
$V_{IN} = 3.7\text{ V}$, Load = 50 mA

Figure 5. Startup by EN



$V_{IN} = 3.7\text{ V}$, $V_{OUT} = 5\text{ V}$, Load = 50 mA to 200 mA

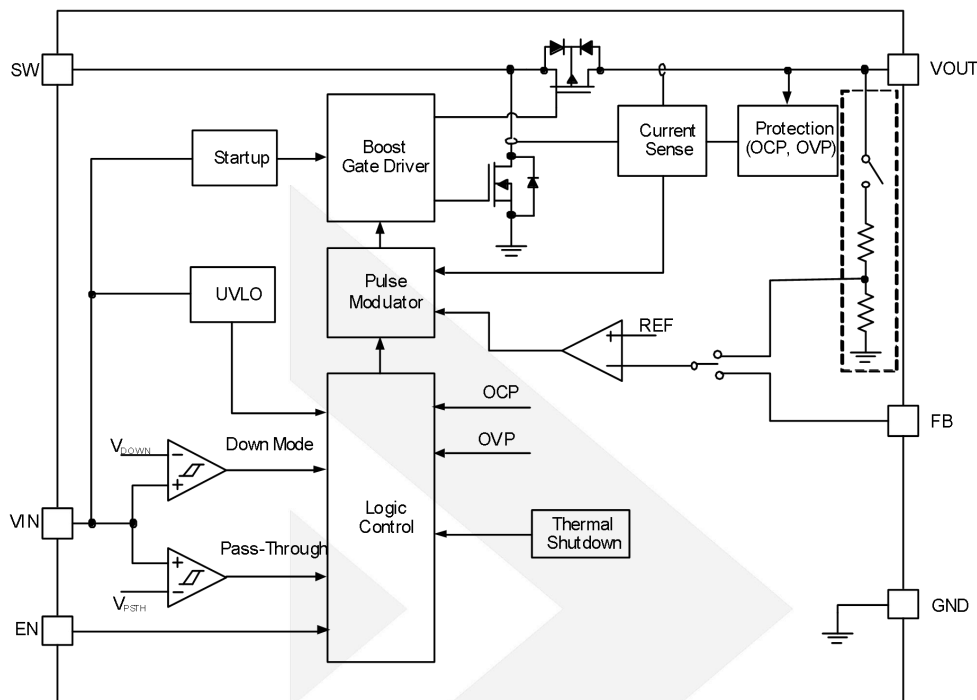
Figure 6. Load Transient



$V_{IN} = 3 / 3.6 / 4.2\text{ V}$, $V_{OUT} = 5\text{ V}$

Figure 7. Efficiency vs. Load Current

Block Diagram



Detailed Description

Overview

The DIO6199 synchronous step-up converter is designed for alkaline battery, coin-cell battery, Li-ion or Li-polymer battery powered systems, which requires long battery running time and tiny solution size. The DIO6199 can operate with a wide input voltage from 1.1 V to 5.5 V. It only consumes 1 μ A quiescent current and can achieve high efficiency under light load condition.

The DIO6199 operates in a Constant Off Time(COT) control scheme with typical 1 A peak switch current limit. The DIO6199 provides the true shutdown function and the load is completely disconnected from the input so as to minimize the leakage current. It also adopts Down Mode and Pass-through operation when input voltage is close to or higher than the regulated output voltage.

Feature Description

Boost Controller Operation

The DIO6199 boost converter is controlled by a Constant Off Time (COT) controller. This controller regulates the output voltage by keeping the off time constant according to input and output voltage, and adjusting the peak inductor current depending on the output load. Since COT is a PFM control mode, the switching frequency is not fixed and is determined by the operation condition. If the required current is lower than the minimum CCM inductor current, the inductor current goes discontinuously to keep the efficiency high under light load condition.

To achieve high efficiency, the power stage is realized as a synchronous boost topology. The output voltage V_{OUT} is monitored via an external or internal feedback network which is connected to the voltage error amplifier. To regulate the output voltage, the voltage error amplifier compares this feedback voltage to the internal voltage reference and adjusts the required offset of the inductor current accordingly.

Under-Voltage Lockout

An under-voltage lockout (UVLO) circuit stops the operation of the converter when the input voltage drops below the typical UVLO threshold of 0.7 V. A hysteresis of 200 mV is added so that the device cannot be enabled again until the input voltage goes up to 0.9 V. This function is implemented in order to prevent malfunctioning of the device when the input voltage is between 0.7 V and 0.9 V.

Enable and Disable

When the input voltage is above UVLO rising threshold and the EN pin is pulled to high voltage, the DIO6199 is enabled. When the EN pin is pulled to low voltage, the DIO6199 goes into shutdown mode. In shutdown mode, the device stops switching and the rectifying PMOS fully turns off, providing the completed disconnection between input and output. Less than 0.5 μ A input current is consumed in shutdown mode.

Soft Start

After the EN pin is tied to high voltage, the DIO6199 begins to startup. At the beginning, if input voltage is lower than approximately 1.6 V, the device operates at the boundary of Discontinuous Conduction Mode (DCM) and Continuous Conduction Mode (CCM), and the inductor peak current is limited to around 200mA during this stage. When the output voltage is charged above approximately 1.6 V, the device starts the Constant Off Time (COT) Mode. If input voltage is higher than approximately 1.6 V, the device starts the Constant Off Time (COT) Mode directly. The current limit threshold in cot mode is reduced within 500 μ s. In this way, the soft start function reduces the inrush current during startup. After V_{OUT} reaches the target value, soft start stage ends and the peak current is now determined by the output of an internal error amplifier which compares the feedback of the output voltage and the internal reference voltage.

Current Limit Operation

The DIO6199 employs cycle-by-cycle over-current protection (OCP) function. If the inductor peak current reaches the current limit threshold I_{LIM} , the main switch turns off so as to stop further increase of the input current. In this case the output voltage will decrease until the power balance between input and output is achieved. If the output drops below the input voltage, the DIO6199 enters into Down Mode. The peak current is still limited by I_{LIM} cycle-by-cycle in Down Mode. If the output drops below 1.6 V, the DIO6199 enters into startup process again. In Pass-through operation, current limit function is not enabled.

Output Short-to-Ground Protection

If input voltage is below 1.6 V, the DIO6199 starts to limit the switch current to 200 mA when the output voltage is below 1.6 V. If input voltage is higher than 1.6 V, the DIO6199 gets into hiccup mode when the output voltage is below $0.1 \times V_{OUT}$. In hiccup mode, the DIO6199 operates 2 ms for every 8 ms. Once the short circuit is released, the DIO6199 goes back to soft start again and regulates the output voltage.

Over Voltage Protection

DIO6199 has an output over-voltage protection (OVP) to protect the device in case that the external feedback resistor divider is wrongly populated. When the output voltage of the DIO6199 exceeds the OVP threshold of 5.8 V, the device stops switching. Once the output voltage falls 0.2 V below the OVP threshold, the device starts operating again.

Down Mode Regulation and Pass-through Operation

The DIO6199 features Down Mode and Pass-through operation when input voltage is close to or higher than output voltage.

In the Down Mode, output voltage is regulated at target value even when $V_{IN} > V_{OUT}$. The control circuit changes the behavior of the rectifying PMOS by pulling its gate to input voltage instead of to ground. In this way, the voltage drop across the PMOS is increasing as high as to regulate the output voltage. The power loss also increases in this mode, which needs to be taken into account for thermal consideration.

In the Pass-through operation, the boost converter stops switching. The rectifying PMOS constantly turns on and low side switch constantly turns off. The output voltage is the input voltage minus the voltage drop across the dc resistance (DCR) of the inductor and the on-resistance of the rectifying PMOS.

With V_{IN} ramping up, the DIO6199 goes into Down Mode first when $V_{IN} > V_{OUT} - 50 \text{ mV}$. It stays in Down Mode until $V_{IN} > V_{OUT} + 0.5 \text{ V}$ and then goes automatically into Pass-through operation. In the Pass-through operation, output voltage follows input voltage. The DIO6199 exits Pass-through Mode and goes back to Down Mode when V_{IN} ramps down to 103% of the target output voltage. It stays in Down Mode until input voltage falls 100 mV below the output voltage, returning to Boost operation.

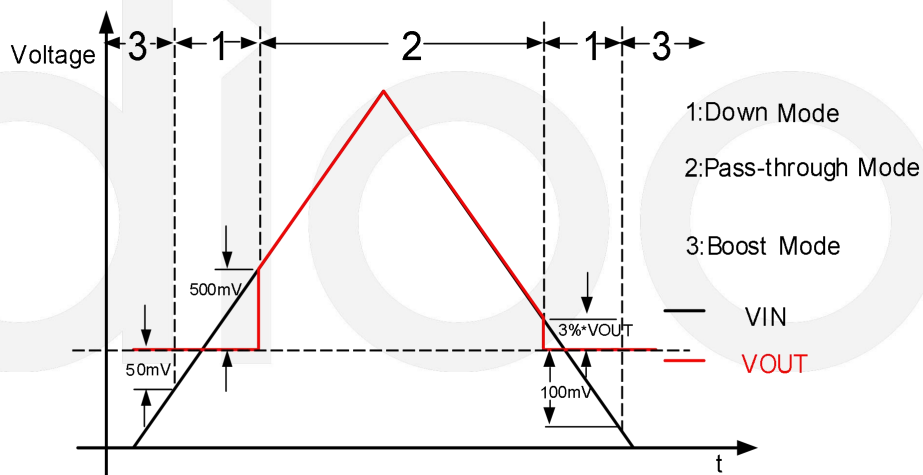


Figure 8. Down Mode and Pass-through Operation

Thermal Shutdown

The DIO6199 has a built-in temperature sensor which monitors the internal junction temperature in boost mode operation. If the junction temperature exceeds the threshold 150°C, the device stops operating. As soon as the junction temperature drops below the shutdown temperature minus the hysteresis, typically 125°C, it starts operating again.

Adjustable Output Voltage

An external resistor divider is used to set the output voltage. The output voltage of the switching regulator (V_{OUT}) is determined by the following equation:

$$V_{OUT} = V_{FB} \times \left(1 + \frac{R_1}{R_2} \right)$$

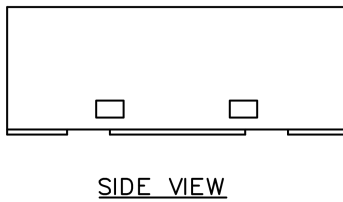
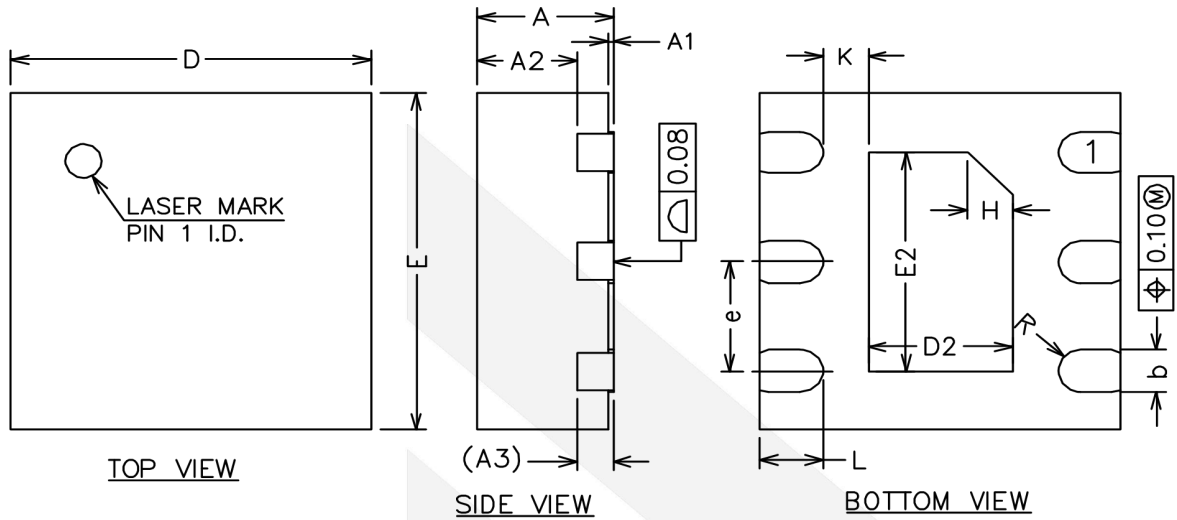
Device Functional Modes**Burst Mode Operation under Light Load Condition**

The boost converter of DIO6199 enters into Burst Mode operation under light load condition. Refer to Boost Controller Operation for details.

Down Mode Regulation and Pass-through Mode Operation

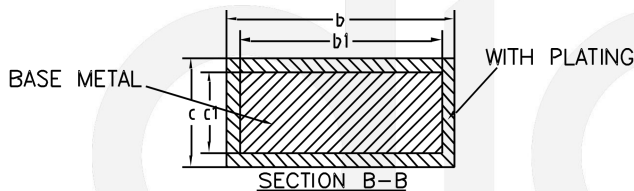
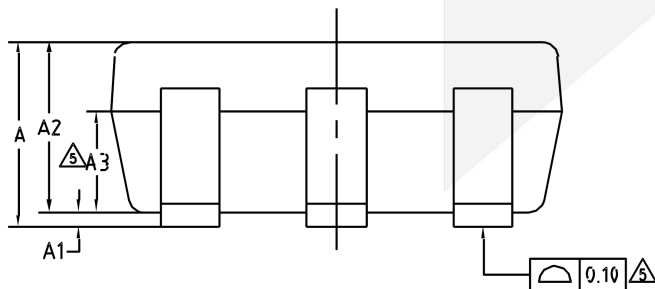
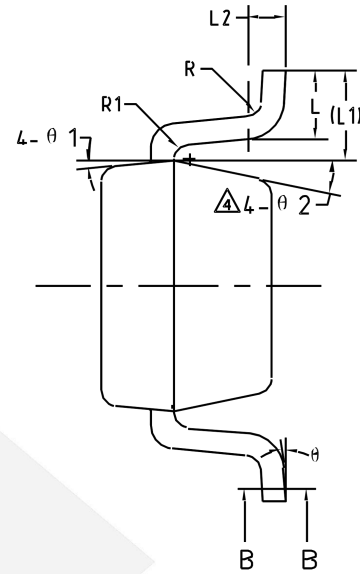
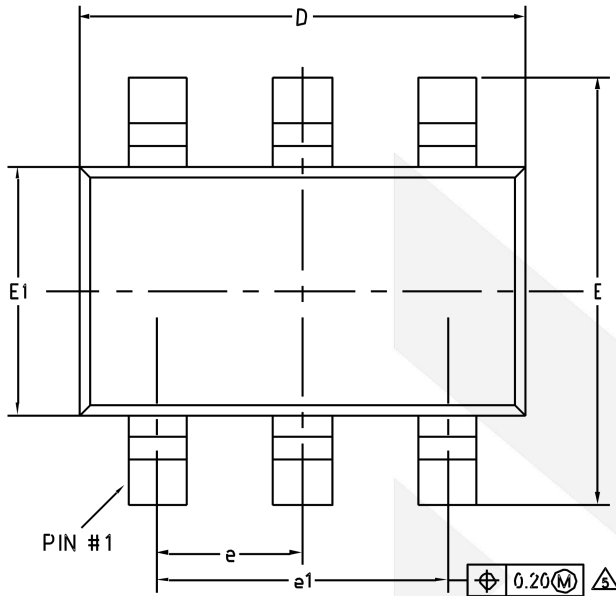
The boost converter of DIO6199 automatically enters into Down Mode or Pass-through mode operation when input voltage is higher than the target output voltage. Refer to Down Mode Regulation and Pass-through Operation for details.

Physical Dimensions: DFN2*2-6



Common Dimensions (Units of measure = Millimeter)			
Symbol	Min	Nom	Max
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
A3	0.20 REF		
b	0.20	-	0.35
D	1.90	2.00	2.10
E	1.90	2.00	2.10
D2	0.70	0.80	0.90
E2	1.20	1.40	1.60
e	0.55	0.65	0.75
H	0.20 REF		
K	0.20	-	-
L	0.30	0.35	0.40
R	0.11	-	-

Physical Dimensions: SOT23-6



Common Dimensions (Units of measure = Millimeter)			
Symbol	Min	Nom	Max
A	-	-	1.25
A1	0	-	0.15
A2	1.00	1.10	1.20
A3	0.60	0.65	0.70
b	0.36	-	0.50
b1	0.36	0.38	0.45
c	0.14	-	0.20
c1	0.14	0.15	0.16
D	2.826	2.926	3.026
E	2.60	2.80	3.00
E1	1.526	1.626	1.726
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
L	0.35	0.45	0.60
L1	0.59 REF		
L2	0.25 BSC		
R	0.10	-	-
R1	0.10	-	0.25
θ	0°	-	8°
θ1	3°	5°	7°
θ2	6°	-	14°

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CONTACT US

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For additional product information, or full datasheet, please contact with our Sales Department or Representatives.

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