

# High Integrated, Single Stage Buck & PFC controller for LED Lighting

#### Features

- Valley turn-on of the MOSFET to achieve low switching losses
- No auxiliary winding and VCC capacitor
- 0.2 V current sense reference voltage leads to a lower sense resistance thus a lower conduction loss
- Reliable short LED and Open LED protection
- Power factor > 0.9
- Compact package: SOT23-5

#### Applications

- Tube lamp & PAR lamp
- Down light & Bulb lamp

#### Descriptions

The DIO8810 is a single stage Buck PFC controller, specifically designed for a high performance non-isolated converter with minimal external components targeting at LED lighting applications.

The DIO8810 drives the Buck converter in the quasi-resonant mode to achieve higher efficiency and keeps the Buck converter in constant on time operation to achieve high power factor.

This chip adopts special design to achieve reliable protection for safety requirement.

# Block Diagram

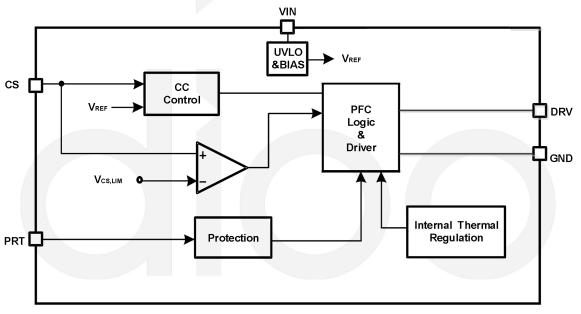


Figure1 Block Diagram



# **Ordering Information**

Order Part Number	Top Marking	RoHS	TA	Package	
DIO8810ST5	YWHHK	Green	-40 to 85°C	SOT23-5	Tape & Reel, 3000

# Pin Assignment

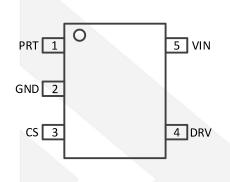


Figure 1 Top View

### **Pin Descriptions**

Pin No.	Name	Description	
1	PRT	Protection pin. Set OVP threshold with three gears.	
2	GND	Ground pin.	
3	CS	Current sense pin. Connect this pin to the source of the primary switch. Connect the sense resistor across the source of the primary switch and the GND pin.	
4	DRV	Gate driver pin.	
5	VIN	Power supply pin.	



#### **Absolute Maximum Ratings**

Stresses beyond those listed under the Absolute Maximum Rating table may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameter		Rating	Unit	
VIN		700	V	
Supply current I <sub>VIN</sub>		15	mA	
CS, PRT, DRV		-0.3 to 7.5	V	
Power dissipation, $P_D @ T_A = 25^{\circ}C$ , SOT23-5		0.6	W	
	θ <sub>JA</sub>	170	- °C/W	
Package thermal resistance, SOT23-5	θ <sub>JC</sub>	130		
Storage temperature range		-65 to 150	°C	
Junction temperature range		150	°C	
Lead temperature range		260	°C	

## **Recommend Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>IN</sub>	VIN voltage	30		500	V
TJ	Operating junction temperature	-40		125	°C

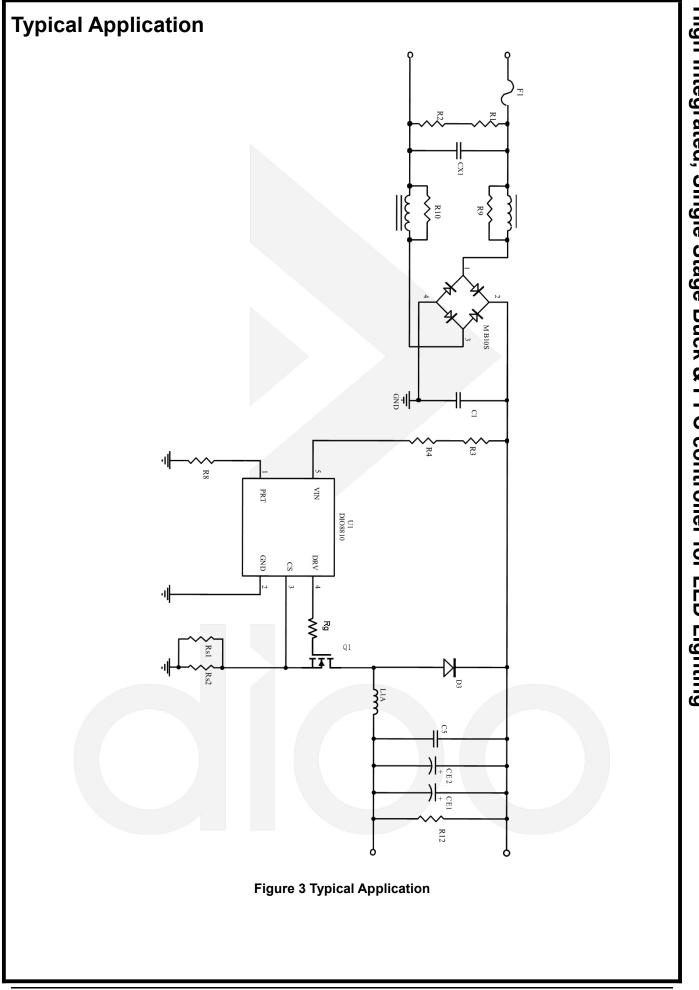


## **Electrical Characteristics**

 $T_A = 25^{\circ}C$ , unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V <sub>IN_ON</sub>	VIN start-up voltage		7	8.5	10	V
IQ	Quiescent current	No switching	150		300	μA
I <sub>VINO</sub>	Operation current	C <sub>L</sub> = 100 pF, f = 15 kHz	200		450	μA
Constant Current	Section					
V <sub>REF</sub>	Internal reference voltage		0.194	0.2	0.206	V
Protection Section	n		1		1	
Vout_ovp	Output OVP voltage threshold	R <sub>PRT</sub> = 470 kΩ		72		V
I <sub>PRT</sub>	PRT pin source current			4		μA
Current Sense Se	ection(Source PIN of integrated MOS	FET)		1	1	
V <sub>CS.LMT</sub>	Current limit reference voltage			1.2		V
PWM Section				1	1	
Ton.max	Maximum on time			25		μs
T <sub>ON.MIN</sub>	Minimum on time			1.2		μs
T <sub>OFF,MAX</sub>	Maximum off time			68		μs
T <sub>OFF_MIN</sub>	Minimum off time			2		μs
f <sub>MAX</sub>	Maximum switching frequency			85		kHz
Gate Driver			1	1	1	
$V_{\text{GATE}\_\text{CLAMP}}$	Output clamp voltage	V <sub>CC</sub> = 20 V		12		V
T <sub>RISING</sub>	Rising time from 10% to 90%	C <sub>LOAD</sub> = 1 nF		210		ns
T <sub>FALLING</sub>	Falling time from 90% to 10%	C <sub>LOAD</sub> = 1 nF		40		ns
Thermal Section					I	
T <sub>SD</sub>	Thermal shutdown temperature			150		°C







### **Application Information**

The DIO8810 is a single stage Buck PFC controller, specifically designed for a high performance non-isolated converter with minimal external components targeting at LED lighting applications.

#### Start Up

After AC or DC supply is powered on, the internal power supply is increasing through HV JFET. Once  $V_{VIN}$  rises up to  $V_{IN_ON}$ , the internal blocks start to work and PWM output is enabled. Once VIN is lower than VIN under voltage lockout, DIO8810 series stops switching.

#### Shut down

After AC supply or DC BUS is powered off, the energy stored in the BUS capacitor will be discharged. Then internal power supply will drop down. Once  $V_{VIN}$  is below the threshold voltage, the IC will stop working.

#### **Quasi-Resonant Operation**

QR mode operation provides low turn-on switching losses for Buck converter.

#### LED Over Temperature Protection

When internal temperature of the chip exceeds 150°C, DIO8810 series decrease LED current to help the chip cooling.

#### LED Open Protection

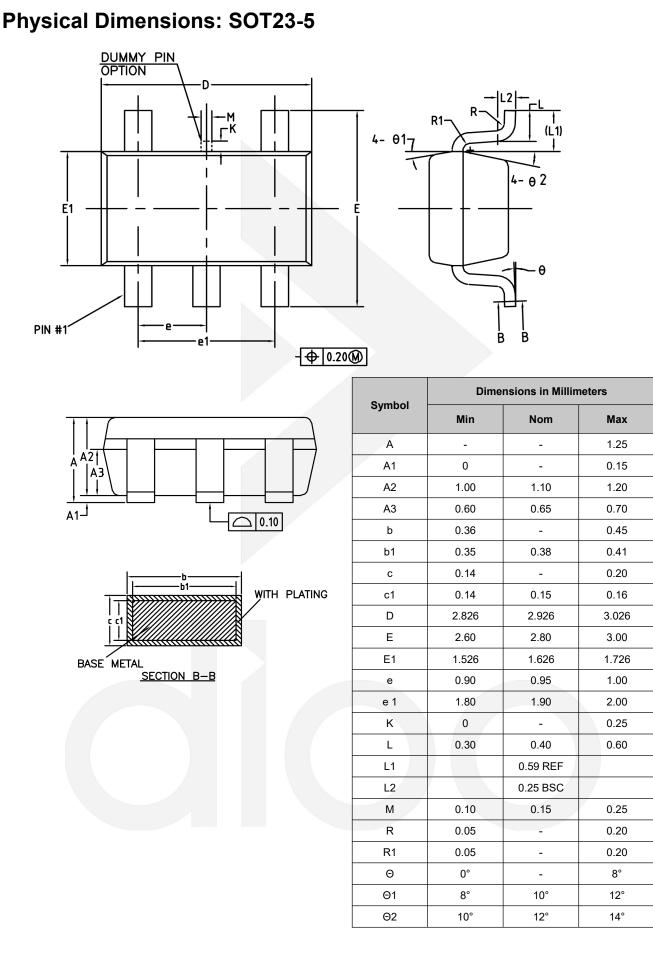
When the load is null or large transient happens, the output voltage will exceed the rated value. The output OVP threshold ( $V_{OUT_OVP}$ ) is regulated by the PRT pin. When output voltage is higher than  $V_{OUT_OVP}$ , the over voltage protection is triggered and the chip stops switching for 850 ms. The system will operate in hiccup mode.

The selection of Rvin will have some impact on the open-circuit protection voltage. The recommended design guidelines for Rvin and OVP are shown in the following table:

Vo_peak (V)	Recommended Rvin ( $\Omega$ )	OVP
42-60	5.1 K	72 V (Typ)
60-100	10 K	112 V (Тур)
100-145	15 K	172 V (Typ)

Vo\_peak (V) is the peak of the output voltage, considering the ripple of the output voltage.







#### **CONTACT US**

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